

ECR #: 18

Title: PME# -- Power Management Pin

Release Date: Feb. 17, 1997

Impact: Change

Spec Version: A.G.P. 1.0

Summary:Power Management on A.G.P. is being supported using the PCI mechanism that has recently been defined in the PIC Bus Power Management Interface Specification.

Background:The PCI SIG is adding a pin named PME# to the PCI specification to support Power Management to PCI.

Change Current Specification as shown:

Table 0-1 PCI signals in relation to A.G.P.	
PME#	Not used for A.G.P protocol but is used by the PCI target interface when being power managed by the OS. Refer to the PCI Bus Power Management Interface Specification for the definition of Power Management Event (PME#) and how a device is power managed by the OS.

Chapter 5

Table 5-1, assign PME# to pin A48 of the connector.