

**ECR #: 15**

**Title: Figure 5-1, 5-2, 5-4 and Table 5-1**

**Release Date: Feb. 10, 1997**

**Impact: Clarification**

**Spec Version: A.G.P. 1.0**

**Summary:**

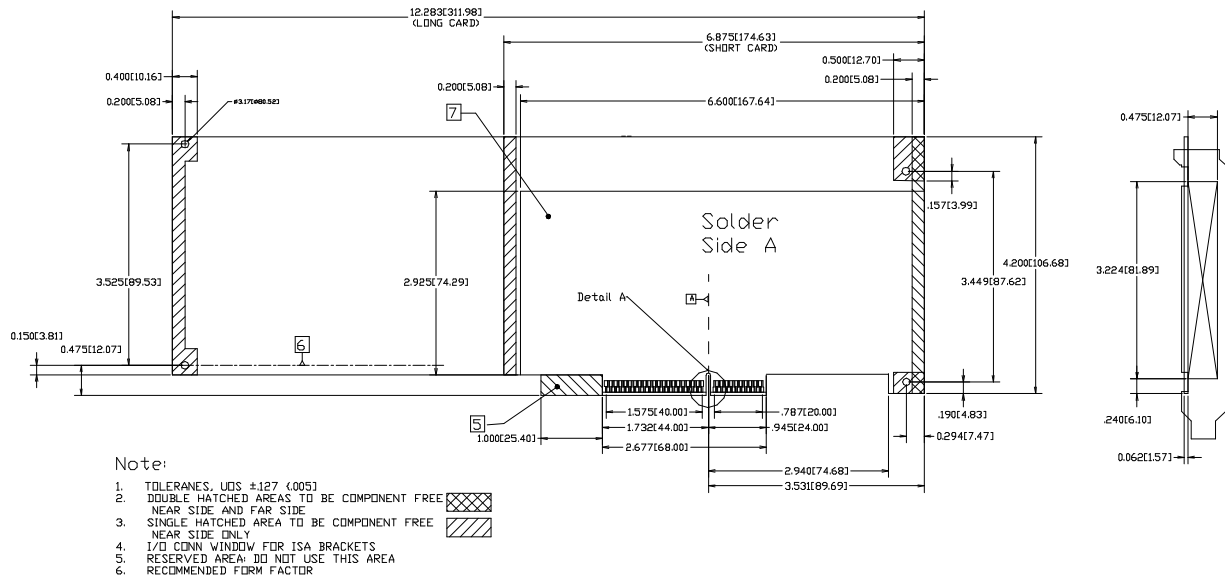
Clarifies discrepancies in Figure 5-1, Figure 5-2, Figure 5-4 and Table 5-1.

**Background:**

**Change Current Specification as shown:**

Item #1

Replace Figure 5-1 with the following:



**Figure 0-1 A.G.P. Card Edge Connector (Side A)**

Item #2

Replace Figure 5-2 with the following:

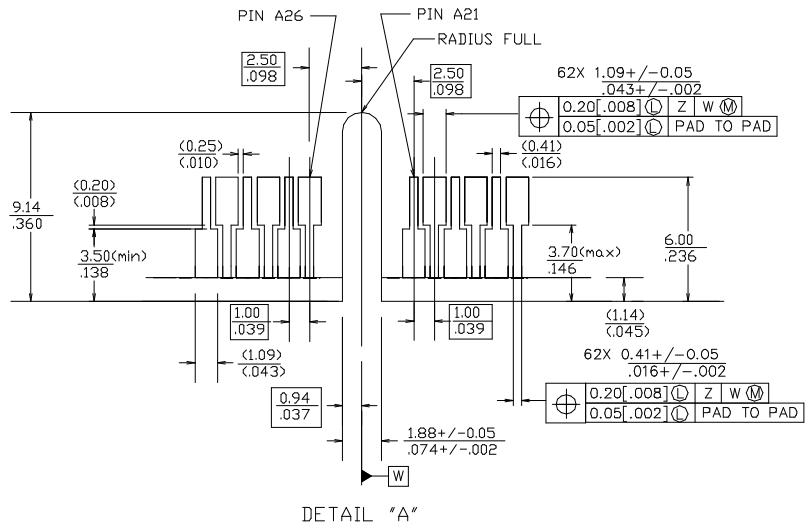


Figure 0-2 :A.G.P. Side-A Card Edge and Finger Layout

Item #3

Add Figure 5-x.

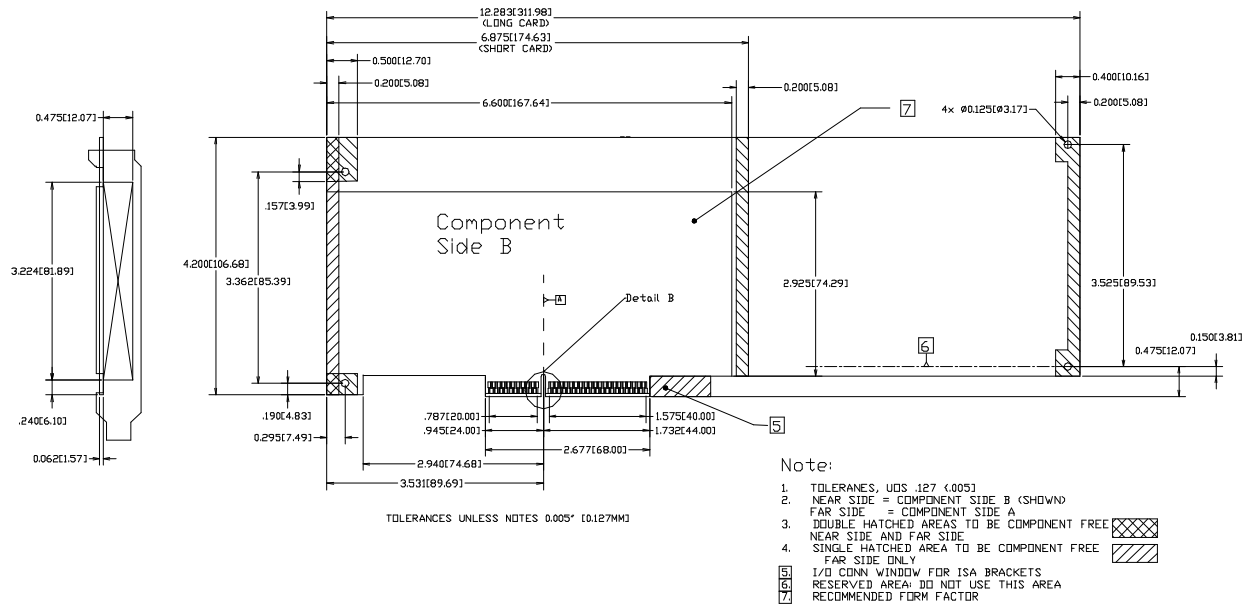


Figure 0-x A.G.P. Card Edge Connector (Side B)

Item #4



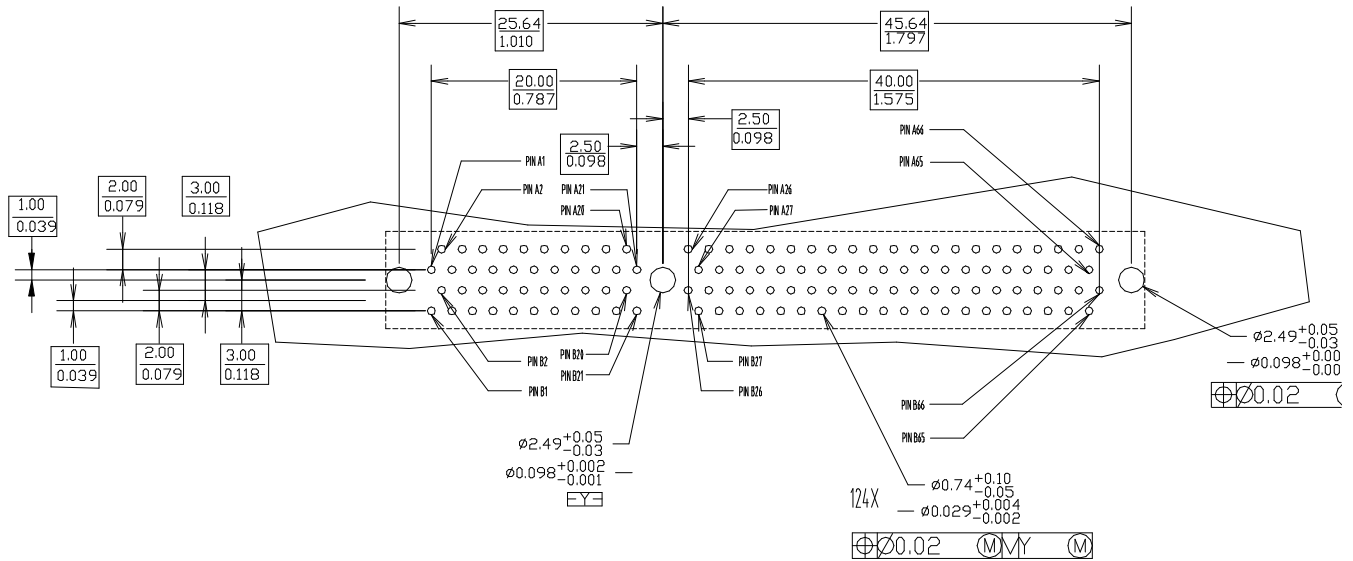
Item #5

Change from:

“Figure 5-4Motherboard Connector footprint and layout deminsions.”

To:

“Figure 5-4Motherboard Connector Footprint and Layout Dimensions.”



Item #6

Replace Table 1 on page 95 with:

	AGP	
Pin#	B	A
1	OVRcnt#	12V
2	5.0V	Reserved
3	5.0V	Reserved*
4	USB+	USB-
5	GND	GND
6	INTB#	INTA#
7	CLK	RST#
8	REQ#	GNT#
9	VCC3.3	VCC3.3
10	ST0	ST1
11	ST2	Reserved
12	RBF#	PIPE#
13	GND	GND
14	Reserved	Reserved
15	SBA0	SBA1
16	VCC3.3	VCC3.3
17	SBA2	SBA3
18	SB_STB	Reserved
19	GND	GND
20	SBA4	SBA5
21	SBA6	SBA7
22	KEY	KEY
23	KEY	KEY
24	KEY	KEY
25	KEY	KEY
26	AD31	AD30
27	AD29	AD28
28	VCC3.3	VCC3.3
29	AD27	AD26
30	AD25	AD24
31	GND	GND
32	AD_STB1	Reserved
33	AD23	C/BE3#
34	Vddq3.3	Vddq3.3
35	AD21	AD22
36	AD19	AD20
37	GND	GND
38	AD17	AD18
39	C/BE2#	AD16
40	Vddq3.3	Vddq3.3
41	IRDY#	FRAME#
42	Reserved	Reserved
43	GND	GND
44	Reserved	Reserved
45	VCC3.3	VCC3.3
46	DEVSEL#	TRDY#
47	Vddq3.3	STOP#
48	PERR#	PME#
49	GND	GND
50	SERR#	PAR
51	C/BE1#	AD15
52	Vddq3.3	Vddq3.3
53	AD14	AD13
54	AD12	AD11
55	GND	GND
56	AD10	AD9

57	AD8	C/BE0#
58	Vddq3.3	Vddq3.3
59	AD_STB0	Reserved
60	AD7	AD6
61	GND	GND
62	AD5	AD4
63	AD3	AD2
64	Vddq3.3	Vddq3.3
65	AD1	AD0
66	Reserved	Reserved