

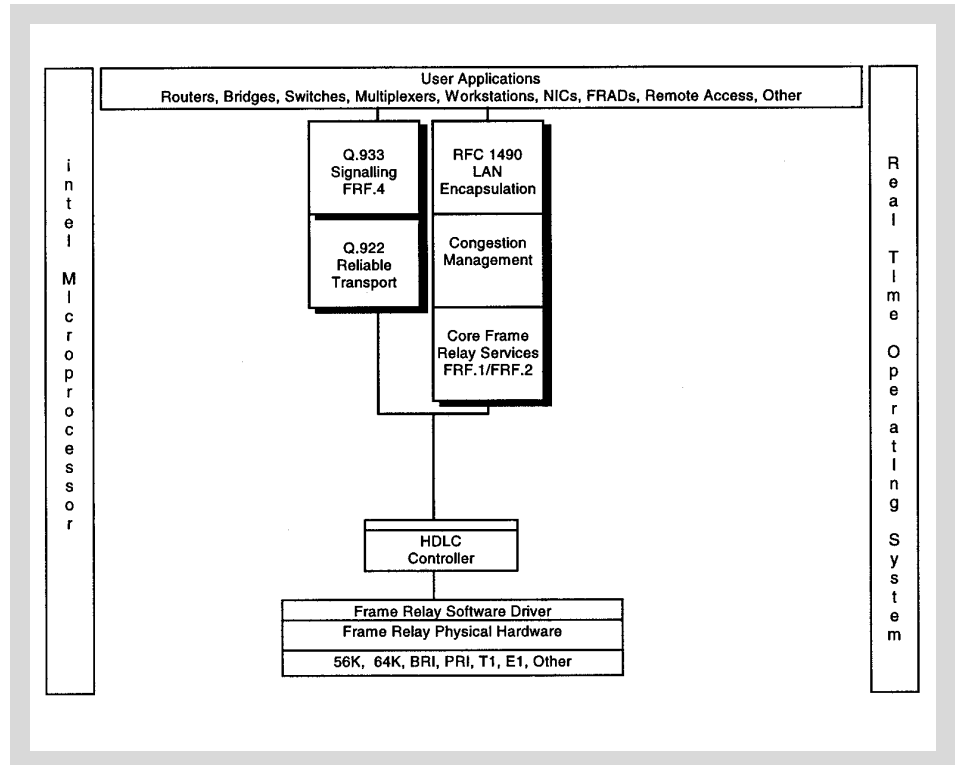
High-Performance Frame Relay* Source Code

- Core Services Frame Relay Per FRF.1 and FRF.2
- LAN Encapsulation Over Frame Relay Per RFC 1490
- Includes LMI Per T1.617 Annex D, Q.933 Annex A, LMI Rev 1.0, and Stratacom
- Frame Relay Signalling Provides SVCs Per Q.933 and FRF.4
- Reliable Signalling Transport and Error Correction Per Q.922
- DTE and DCE Support With Local Switching
- Comprehensive Ingress and Egress Congestion Management With Data Throttling
- High Performance and Throughput
- Operating System Independent
- Compatible With Complete i960® Processor Product Line

Harris & Jeffries High-Performance Frame Relay* product line provides communications manufacturers and OEMs with a fast and easy way to implement Frame Relay. Manufacturers can use this powerful source code in conjunction with Intel microprocessors, HDLC controllers, and real time operating systems to design and develop frame relay NIC cards, FRADs, switches, routers, hubs, multiplexers, test equipment and other frame relay products quickly and easily.

High-Performance Frame Relay provides core frame relay services per FRF.1 and FRF.2. In addition this powerful package includes four different versions of LMI, RFC 1490 LAN encapsulation over frame relay, and superior congestion management. This congestion management has a throttling feature which allows the frame relay data to be throttled down when the committed information rate (CIR) is exceeded and then return to its normal speed when the congestion is eliminated.

High-Performance Frame Relay Signalling provides SVC capability per FRF.4 and Q.933. The signalling software uses innovative techniques to provide the



fastest performance and throughput in the industry. It also has an end-to-end reliable transport protocol, Q.922, that ensures that the signalling information is passed reliably through the network.

These packages are compatible with Harris & Jeffries Soft-ATM* Frame Relay Network Interworking, and Frame Relay Service Interworking so that users can put frame relay data over ATM and provides a migration path to higher speeds and to ATM. Multiple Harris & Jeffries software packages can be integrated together to reduce the porting effort.

High-Performance Frame Relay products are compatible with all i960 class microprocessors. Other Harris & Jeffries source code products include Soft-ATM, X.25 and PAD, SDLC, LLC, QLLC, and SNMP.

CONTACT:

Harris & Jeffries
 888 Washington Street
 Dedham, MA 02026
 Phone: (617) 329 3200
 FAX: (617) 329 4148
 e-mail: info-intel@hjinc.com
 WWW: <http://www.hjinc.com>

