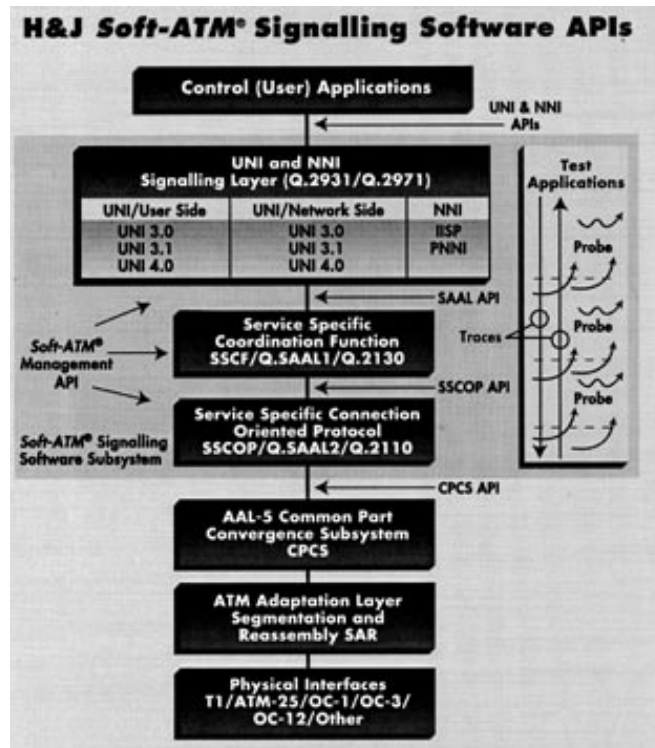


Soft-ATM* Source Code

- ATM Signalling Includes UNI 3.0, UNI 3.1, UNI 4.0, IISP, and PNNI
- ATM ILMI Network Management and Address Registration
- ATM LAN Emulation Client (LEC) and LAN Emulation Server (LES)
- ATM LAN Emulation Server Includes LES, LECS, and Bus
- Classical IP Over ATM Via RFC 1577
- Frame Relay to ATM Network Interworking (FRF.5)
- Frame Relay To ATM Service Interworking (FRF.8)
- PNNI Routing
- High Performance Throughput and Operating System Independent
- Includes Test Applications, Diagnostic Traces, Loopbacks, and Probes
- Compatible With Complete i960® Processor Product Line and i960 RP I/O Processor Reference Design



Harris & Jeffries Soft-ATM* product line provides communications manufacturers and OEMs with a fast and easy way to implement ATM. Manufacturers can use this powerful source code in conjunction with Intel microprocessors, ATM SAR chips, and real time operating systems to design and develop ATM NIC cards, switches, routers, hubs, multiplexers, test equipment and other ATM products quickly and easily.

Soft-ATM Signalling provides SVC support and complies with The ATM Forum UNI 3.0, 3.1, 4.0, IISP, and PNNI specifications. It uses innovative techniques to provide the best throughput and call per second rate in the industry. Soft-ATM ILMI can be added to provide an in-band management channel and automatic address registration over the ATM network.

LAN Emulation Client and LAN Emulation Server provide LAN encapsulation of legacy protocols such as IP, IPX, APPN, NETBIOS, and others over the

ATM network. Classical IP Over ATM provides a more efficient way to transport only IP traffic via RFC 1577.

Frame Relay to ATM Network Interworking, FRF.5, provides a way to transport frame relay over ATM and back to frame relay at the remote end. Frame Relay To ATM Service Interworking, FRF.8, provides a method to transport frame relay over ATM without needing to convert back to frame relay at the remote end.

Harris & Jeffries Soft-ATM product line includes test applications, diagnostic traces, probes, and loopbacks to provide a hassle-free integration effort. Multiple packages can also be integrated together to make the porting of multiple packages easier. Harris & Jeffries provides an interoperability guarantee so that users can be assured that the ATM code will work with other products that comply with The ATM Forum.

Soft-ATM products are compatible with all i960 class microprocessors and the i960 RP processor Intelligent I/O reference design. Other Harris & Jeffries source code products include High-Performance Frame Relay core UNI and NNI software, High-Performance Frame Relay Signalling, X.25 and PAD, SDLC, LLC, QLLC, and SNMP.

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