

## RISCom/TCi High Performance ATM Adapter For The PCI Bus

### Feature Summary

- 40 MHz i960® CF RISC Processor MPU
- 32-Bit PCI Bus Mastering With FIFO Interface
- Advanced VLSI ATM Chip Set For SAR Functions
- Support For AAL 3/4, AAL5 & Transparent Mode
- Media Modules Include DS3, OC1, OC3, UTP5, TAXI and Other Daughter Cards

### Media Module Options

- SINGLE MODE OC-3
- MULTI-MODE OC-3
- DS3 & E3
- UTP3 & UTP5
- Custom Interfaces Available

### Applications

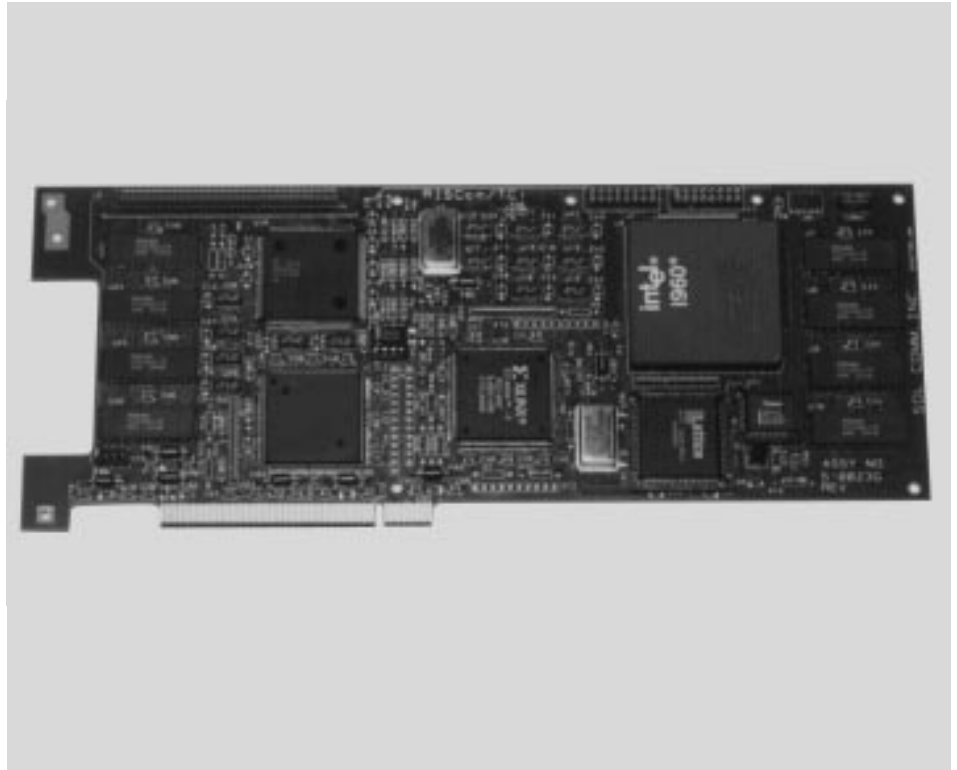
- UNI & NNI Interface
- ATM Test Equipment
- VIDEO Delivery
- ATM Cell Generator
- Routers & Bridges

### Software Support

- ATM Forum UNI 3.0 Compliant
- Q.2931 Signaling
- LAN Emulation Per ATM Forum
- IP Over ATM Per RFC1577 and RFC1483
- Drivers For DOS and UNIX
- SNMP MIB For Status, Cell Statistics, Error Statistics & VPI/VCI Information

SDL's TCi (Turbo Cell interface) card is a high performance ATM adapter for the PCI bus. The TCi is ideally suited for applications which include video (MPEG-1 & MPEG-2) transport, ATM Cell generation, monitoring and analysis applications. The hardware and software components of the product comply with the ATM Forum's UNI 3.0 specification requirements.

The TCi features an Intel i960® RISC processor coupled with an advanced VLSI ATM chip set which supports AAL3/4, AAL5 and Transparent Cell Transfer modes. The TCi includes a Cell Buffer



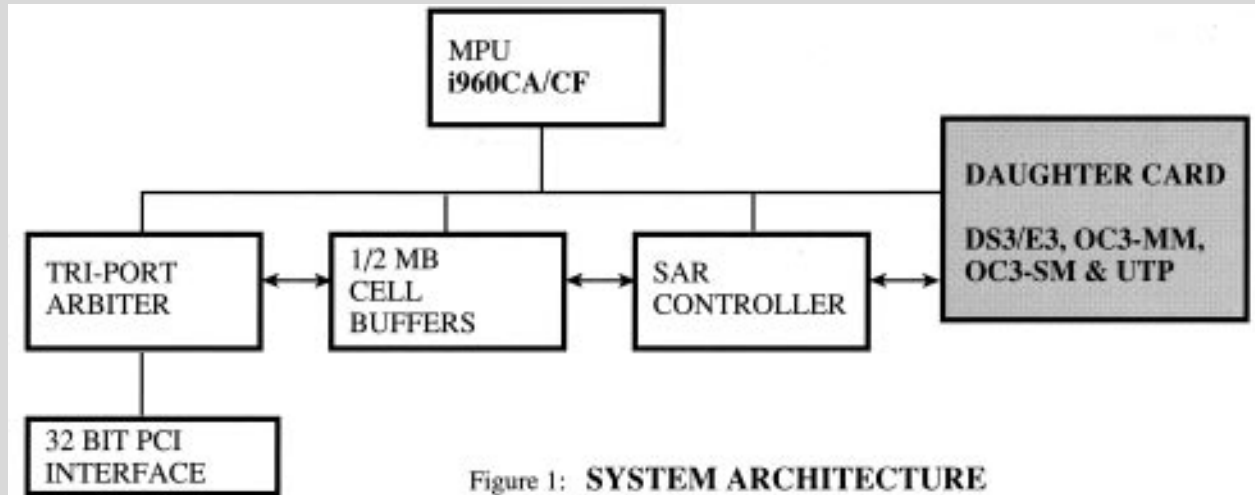
that is expandable from .5 Mbytes to 8 Mbytes. The TCi features a high performance PCI bus interface controller which supports burst operations at 132 MBps across the PCI bus. The PCI controller contains a 96 byte bi-directional FIFO which can be partitioned via software into a symmetrical or asymmetrical FIFO or even configured for Receive Only or Transmit Only applications. The architecture for SDL's family of ATM adapters for the PCI Bus is illustrated in Figure 1.

The physical interface front-end is a modular daughter card design which supports a comprehensive list of media modules which include DS3/E3, OC-3, UTP3 and UTP5. This modular design supports the design of custom media modules which can be configured for receive/transmit only applications such as Network Analysers and Cell Generators.

### CONTACT:

SDL Communications, Inc.  
46 Eastman Street  
Easton, MA 02334-1303  
Phone: (508) 238-4490  
FAX: (508) 238-1053  
e-mail: [sdl@world.std.com](mailto:sdl@world.std.com)



**SPECIFICATIONS****PCI INTERFACE**

<b>Adapter Type:</b>	32-bit PCI Bus Master
<b>Bus Performance:</b>	132 MBps Burst 60 MBps Sustained Plug & Play via PCI S16 specification

**ATM ENGINE**

33 MHz i960® CF (Scalable to i960 CA) RISC Processor 1/2 Mbyte Code RAM 256 Kbyte Flash ROM Advanced VLSI SAR Chip Set 1024 VC Support 1/2 - 8 MB Cell Buffer
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**MEDIA MODULE OPTIONS**

<b>DS3:</b>	44.736 Mbps, PLCP per TA-TSY-000773 & TA-TSY-000774
<b>E3:</b>	34.236 Mbps, PLCP per ETSI TNA (91) 17 & 18

**Optical:**

SONET OC-3 at 155.52 Mbps,  
Single & Multi-mode Signal format  
per ANSI T1.105 Wavelength  
1310nm nominal

**Copper:**

TAXI, CAT5 UTP Cable

**Compliance:**

ATM Forum Version 3.0 UNI  
Specifications ITU I.361

**Power Requirements:**

2.0 Amp @ +5 Volt  
50mA @ +12 Volts  
50mA @ -12 Volts

**ENVIRONMENTAL REQUIREMENTS**

<b>Operating Temperature:</b>	0 to 55 degC (32 to 131 degF)
<b>Relative Humidity:</b>	To 90% without condensation