

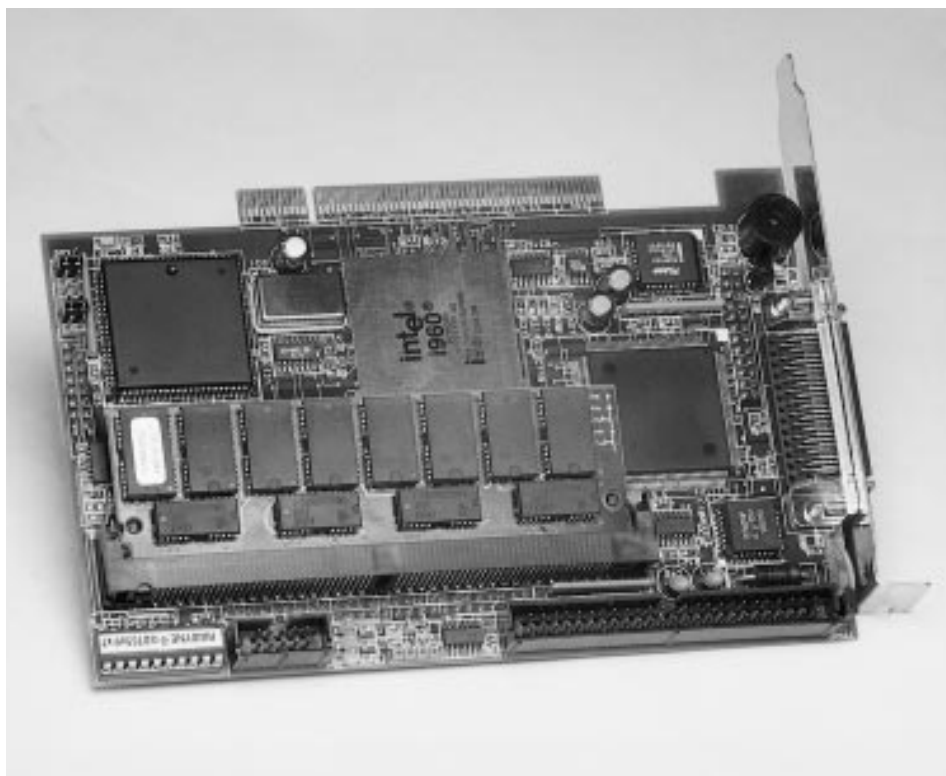
MPR Hardware Disk Array Controller

- PCI 2.x Compatible
- 1, 2, 3 or 5 Wide or Wide & Ultra SCSI Channels Up to 40 MB/Sec. Synchronous Data Transfer Rate
- Software-Switchable and Automatic Active SCSI Termination (Low Byte and High Byte Can be Terminated Separately)
- Realtime Multi-Tasking Firmware RAIDYNE*
- Supports RAID Levels 0, 1, 4, 5, 10
- Dual Connector System With 1, 2 and 3-Channel Controllers Internal 16 Bit and 8 Bit Connectors
- 2 External 68 Pin Connectors
- Supports Hot Plug and Hot Fix (Private or Pool)
- Up to 64 MB Cache RAM With Intelligent Cache Algorithm
- Fully Capable of DMA Bus Mastering
- Automatic Failed Drive Detection
- Acoustical Alarm
- Automatic Rebuild
- Firmware and BIOS in Flash RAM
- Supports All Major Operating Systems
- SNMP and MHS Support
- Monitoring Utility
- Supports SAF-TE and DEC Fault Bus

All ICP Disk Array Controllers are designed to comply with the highest security requirements. One of the vital questions is:

Where is the information about the Disk Array and its members?

If it is on the controller (e.g., in NVRAM) or in a file, there is a high risk of losing this information or running into inconsistencies which then lead to negative consequences. With all ICP Disk Array Controllers, the configuration information is stored twice on each disk drive of a Disk Array. Another important point is the clear and precise detection of a failed or replaced drive.



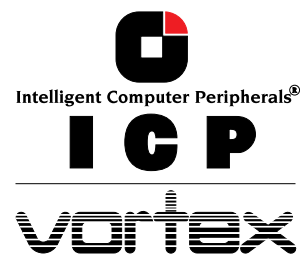
The new ICP Disk Array Controllers utilize the i960® RISC processor technology. There are different models concerning the number of single-ended SCSI Channels (1, 2, 3 or 5) and SCSI Bus technology (FAST, FAST-20, 8 bit, 16 bit). They are suitable for applications demanding high performance and sustained data availability.

ICP Disk Array Controllers offer easy access to modern RAID technology because the user does not require knowledge of the technology prior to use. The setup software includes an express setup function which automatically carries out entire installation and delivers a fully operational RAID system.

ICP Disk Array Controllers can also control non-direct access SCSI devices (e.g., CD-ROM, DAT, DLT, MO, etc.). These devices are either managed directly by the operating system or through ASPI.

AVAILABILITY:
1/96

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SPECIAL FEATURES

CPU	Intel i960® RP I/O processor
Architecture	General 32-bit architecture
Technology	Multiprocessor RISC technology
RAM	Up to 64 MB cache RAM (0 MB RAM supplied)
Cache	Expandable with a 72 pin, 32/36 bit, PS/2 SIMM
Cache Algorithm	Intelligent cache algorithm with Look- Ahead and Delayed-Write functions
Host Bus	100% PCI bus compatible; Boot ROM, PCI-Target and PCI-Master, 32 bit, 33 MHz
DMA	Fully capable of DMA bus mastering
Connector System	Dual connector system present on 1-, 2- and 3 channel wide – and ultra SCSI Controllers
Updating	Flash-RAM technology
Standards	Compliant to the CE and FCC standards
Fault Bus	SAF-TE and intelligent fault bus support

OPERATING SYSTEM SUPPORT

- DOS from Version 3
- OS/2 V 2.x & 3.x
- Novell NetWare V 3.x & 4.x
- UnixWare V 1.1 & 2.x
- Windows NT 3.51 & 4.00 (B. 1381)
- Windows 95
- Interactive Unix V/386 Rel. 3.2
- V 3 & 4.x
- SCO Unix V/386 Rel. 3.2 V 4.x
- SCO Unix V/386 Rel. 3.2 V 5.x
- QNX v 4.22
- Linux v 2.0.0