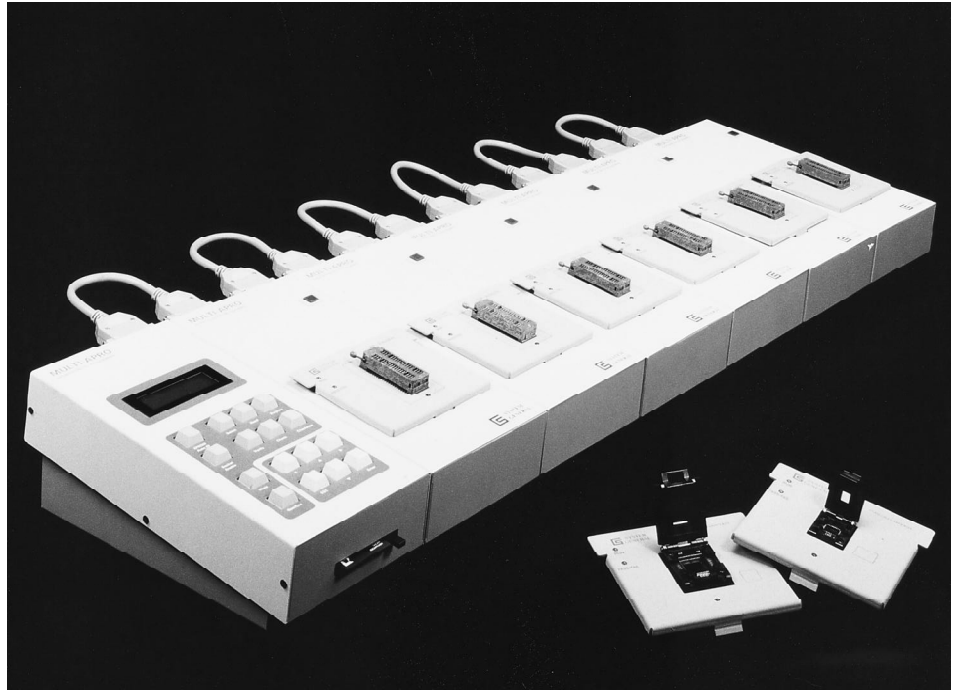# Multi-APRO

- Engineering/production model universal programmer
- Innovative independent-socket technology for ultimate flexibility
- Start with single-socket universal engineering programmer, expand to production when needed
- Operates stand-alone or via Windows*-style PC control (SW included)
- Optional jobs management production SW
- 8-Mbit RAM standard (per module), upgradable to 64-Mbit
- Supports Intel 8-bit and 16-bit microcontrollers
- Intel-evaluated
- Insertion-check, auto erase, blank check, verify
- SW/FW updates via 24-hour BBS (FW stored on flash card)

System General's Multi-APRO is an extremely advanced programming system for processing Intel Flash memory components for engineering or production. Support for all package styles and component families is comprehensive and ongoing. Based on a 48 Universal Pin-Driver architecture, the Multi-APRO is the ideal tool for design or manufacturing when working with programmable devices. The system can be configured with a single socket for R&D and then expanded as the application turns to production. As many as eight modules can be linked to one controller so that programming can occur 8-at-a-time asynchronously! Stand-alone capability allows the user to program from a master device, or, tied to a PC, files can be downloaded from floppy, hard-drive or network. Default programming algorithms include erase, program, verify (or marginal verify). Device-specific algorithms are stored in the PC and on a flash PCMCIA card for firmware. When updates are obtained via electronic transfer, the system will in-circuit program the flash for instant FW updates (no messy switching out of EPROM FW!).

The Multi-APRO is reasonably priced for your application, be it engineering, low-volume or high-volume production. Mouse-driven software allows for easy to apply engineering/editing features.

Use of only approved algorithms assures optimum yields, and System General's patented auto-mapping technology provides for the fastest possible programming times and therefore highest throughput. Asynchronous operation means that as soon as the first device is inserted programming begins, when the nth socket is filled, programming in socket one is complete, and so on.

Easy-to-use engineering features allow for convenient editing of data. Additional capabilities include swap-shuffle-split and even serialization! Optional high/low V_{cc} verify can help insure board-level reliability. Boot-block secure features of Intel Flash memory devices are handled via pop-up windows to reduce operator error.

INTEL FLASH MEMORY SUPPORTED:

28F010, 28F001BX, 28F020, 28F002BC, 28F002BL, 28F200BV, 28F002BX, 28F200BL, 28F200BV, 28F200BX, 28F200CV, 28F004BE, 28F004BL, 28F004BV, 28F004BX, 28F004SC, 28F400BL, 28F400BV, 28F400BX, 28F400CE, 28F400CV, 28F008BE, 28F008BV, 28F008SA, 28F008SC, 28F800BV, 28F800CE, 28F800CV, 28F016SA, 28F016SC, 28F016SV, 28F016XD, 28F016XS, 28F032SA

AVAILABILITY:

Now

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