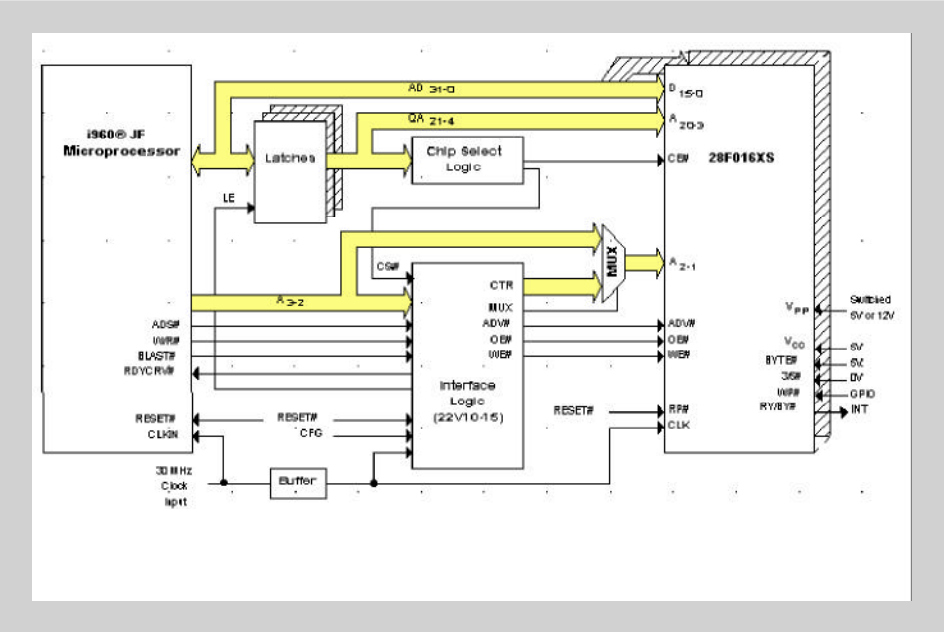
Reference Designs

- Subsystem designs focused around processor/controller and flash memory
- Simulated and/or lab-tested
- Include schematics, logic files, timing analysis, S/W and/or documentation
- Saves design time



Reference designs are CPU/memory subsystem designs, focused around one CPU, one flash memory device, and the glue-logic that connects them. These designs include logic schematics, logic files for programmable logic arrays, timing analysis details, software, and technical documentation. Many reference designs have been lab-tested; all have been simulated. The following reference designs are currently available:

– 28F016XS with the i960® family (960 CA, 960JF-standard, 960JF-optimized, and 960 KB),

– 28F016XS with the i486™ family (both standard and optimized)

– 28F016XS with non-Intel CPUs.

– 28F016XD with the i386™ EX microprocessor

– 28F016XD-based SIMMs.

Intel will be developing and releasing additional reference designs for other flash memories and microprocessors throughout the coming quarters. These designs help you save time and money; they can be dropped into your end system designs, with all the work already done! They are available from Intel free of charge.

INTEL FLASH MEMORY SUPPORTED:
28F016XD, 28F016XS

Note: As additional reference designs become available, Intel will add them to the WWW and BBS.

AVAILABILITY:
Now

CONTACT:
See Appendix C