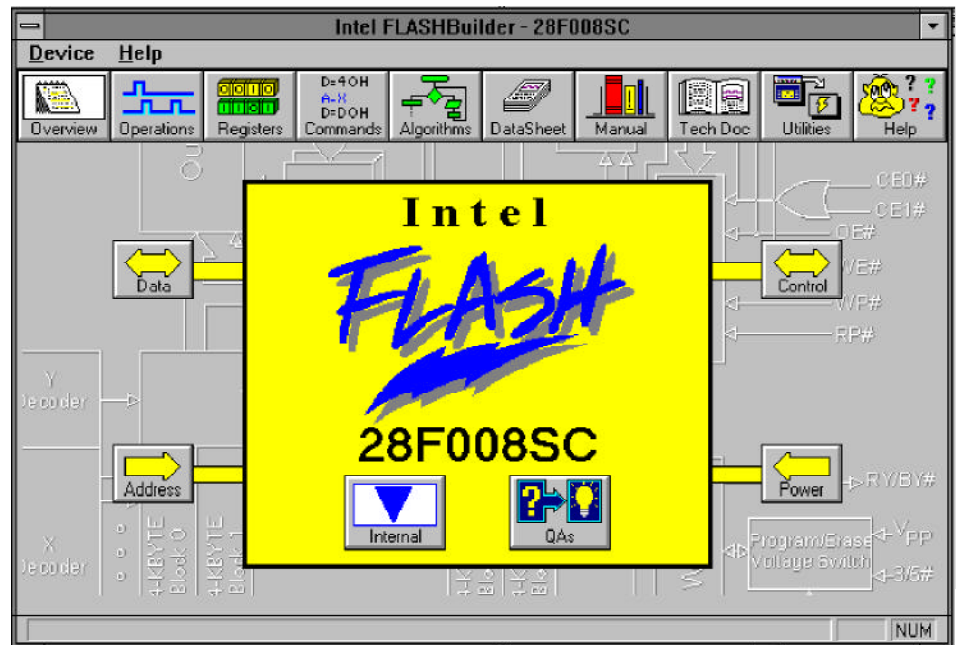# FLASHBuilder FlashFile™ Memories

- Power Profiler Utility
- Product Decoder Utility
- Flash Cycling Utility
- Source code for program and erase algorithms in both C and ASM86
- 16-Mbit Product Family User's Manual
- Family datasheets and application notes
- Timing and waveform specifications
- Flash memory overview - a high level description of flash technology and product applications
- Windows* 95-compatible

FLASHBuilder FlashFile™ Memories is a Windows-based software tool developed to facilitate the understanding of Intel's FlashFile memory components. Through an easy-to-use graphical user interface, you are able to speed time-to-market by choosing the optimum FlashFile memory component for your specific application.

FLASHBuilder FlashFile Memories supports Intel's 28F016SA/SV, 28F008SA, 28F016SC/SC-L, 28F008SC/SC-L and 28F004SC/SC-L FlashFile memory components. Ease-of-design is facilitated by FLASHBuilder FlashFile memories through its intuitive interface to source code for program and erase algorithms in both C and ASM86. These can easily be edited to customize a design.

Also an aid for system design, FLASHBuilder FlashFile Memories provides several custom utilities. The Power Profiler Utility provides information on power consumption based on operating conditions and the number of devices in the system. The tool can also generate a graphical view of flash memory consumption versus the total system power budget. The Product Decoder Utility enables the user to decipher the sometimes cryptic nomenclature of Intel's many flash memory devices. After using this product, users will be able to distinguish package, architecture and tempera-

ture identifiers taking the guesswork out of product ordering codes. While the Flash Cycling Utility provides a quantitative profile of flash memory cycling due to periodic system updates, it allows you to change system parameters and determine how that will affect cycling capabilities.

FLASHBuilder FlashFile Memories features a graphical representation of device functional blocks within the flash memory architecture as well as quick access to information about device operations, commands, algorithms and registers. For example, the command dialog box displays on a single screen the command set and corresponding hexadecimal values, bus-cycle counts for each command, and status register output indicating a successful operation.

Access to technical information and specification is easy. Through FLASHBuilder FlashFile Memories' on-line documentation you have instant access to associated application notes, datasheets and the 16-Mbit Product Family User's Manual all in hypertext format.

AVAILABILITY:

2H'96

CONTACT:

See Appendix C

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