PRINTERS

QuickPrint – High Performance Integrated Printer Coprocessor

- Single LSI Device For Total Printer Control
- High-Speed Network Printers Up to 100 ppm
- All-Pages-Print* With Memory Reduction Technology (MRT*)
- Operating Frequency Up to 33 MHz
- Efficient 32-Bit Dual Bus Architecture
- Fourth Generation Peerless Graphics Execution Unit (GEU)
- Print Engine Video Controller 132 MHz Video Rate
- I/O for Direct Printer Engine Communication
- High Performance IEEE 1284
 Parallel Port With Dedicated DMA
- ROM, DRAM and I/O Controllers

The QuickPrint 1700 is an LSI coprocessor designed to provide cost reduction for high performance network printer and mid-range multi-function peripheral controllers. The QuickPrint 1700 also provides cost effective solutions ranging from 12 ppm up to 100 ppm at 600 x 600 dpi, and up to 24 ppm at 1200 x 1200 dpi. A broad range of target applications are supported with direct interfaces to Intel's i960[®] Kx, Jx, Cx, or Hx processors.

The QuickPrint 1700 reduces costs by limiting the memory required to print complex pages and by integrating the required features of a high performance printer controller. The memory savings is provided by Peerless Memory Reduction Technology (MRT*) which is supported by the Peerless Graphics Execution Unit (GEU). The GEU provides patent pending lossy and lossless compression and decompression, in addition to a compact display list page representation. Integrated components include the Peerless GEU and controllers for ROM, DRAM, I/O, print engine video and communications, an IEEE 1284 parallel port and a serial port.

The high performance CPU clock rate, high speed video rate, high throughput GEU, and dual bus architecture contribute



to the high performance of the QuickPrint 1700. The clock rate allows use of high speed processors for printing complex graphics pages at engine speed. The video rate enables support for print engines up to 100 ppm. The dual bus architecture increases performance by enabling the CPU, GEU and PVC to run in parallel.

The IEEE 1284 parallel port can support up to 4 MB per second forward transfers in compressed ECP mode and up to 2 MB per second in reverse ECP transfers. The energy efficient design typically consumes less than 300 mWatts and is ideal for Energy Star compliant designs.

PROCESSORS SUPPORTED:

i960 Jx, Kx, Cx and Hx Processors

AVAILABILITY: Now

CONTACT:

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