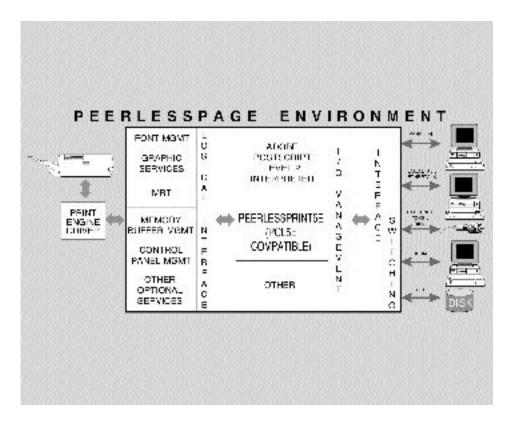
PeerlessPrint5E (PCL 5e Emulation)

- Complete HP LaserJet 5P Compatible Implementation
- Variable Resolution Support For 1200, 600, 300, 150, 100, 75 dpi etc.
- ROP 3 Extensions
- Support for AGFA, UFST* and Bitstream Four-In-One Rasterizers
- Multiple Font Support: TrueType, Intellifont
- Full PJL Command Set
- Supports IEEE 1284/Bidirectional Parallel
- 2 Mbytes 600 dpi For MRT
- Available as Standalone or PeerlessPage Package

PeerlessPrint5*E* is Peerless' HP LaserJet 5P compatible PCL 5e emulation. To develop this emulation, Peerless leveraged its variable resolution core product, PeerlessPrint5*VR* (which has supported 600, 300, 200 and other dpi resolutions since it first began shipping in early 1992). PeerlessPrint5*E* is the first full implementation of the LaserJet 4 language emulation from a printer technology company on the market.

Peerless has expanded the PeerlessPrint5E PDL to provide compatibility with the LaserJet 4Si, 4L, 4P, 4 Plus and 5P printer features. These printers each employ different modifications to the basic PCL5 language. These modifications include specific raster graphics operation commands to better support Windows graphics, various image adapting and compression methods, economode, custom page size, different font sets, etc. Laserjet printers also support an evolving array of PJL commands for bidirectional status and configuration dialog with the host, including emulation selection, paper handling, 300 and 600 dpi resolution choices and hardware specific features.

PeerlessPrint5*E*, together with the PeerlessPage Imaging Operating System, is available with Peerless' proprietary Memory Reduction Technology (MRT) to



provide robust 1200 dpi printer implementations in 6 Mbytes RAM, 600 dpi printer implementations in 2 Mbytes RAM and 300 dpi implementations in 1 Mbyte RAM.

PeerlessPrint5E can run with standard HP drivers when available from Windows or other applications, or use LaserJet III drivers. Peerless OEM customers can also provide a custom driver so that users can exploit specific value-added features on their printers.

PeerlessPrint5*E* interpreter code is available in source for or as part of turnkey development projects. Peerless'
PeerlessPrint5 family source code is portable and written in the C language and has been ported to Intel's processor family.

PROCESSORS SUPPORTED: i960® Processor Family

AVAILABILITY:

Now

CONTACT:

Peerless Systems Corp. 2381 Rosecrans Avenue

El Segundo, CA 90245

Attn: Tom Cocotis

Product Manager

Phone: (310) 297-3278

FAX: (310) 536-0058

For international contacts see Appendix B.

