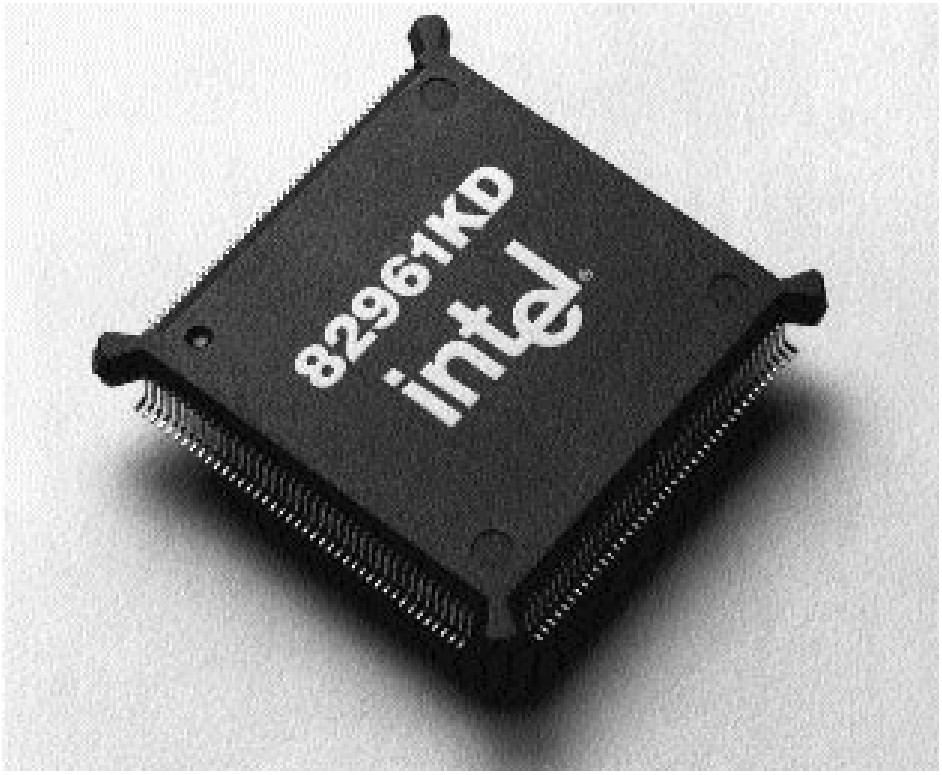
82961KD Printer Coprocessor

- High Integration i960® Microprocessor Banding Coprocessor Printer Controller
- Direct Interface to the 32-Bit i960 KA or KB Embedded Processors
- On-Chip BitBLT Accelerator to Provide Fast Printer Performance
- Real-Time Compressed Display List Processing
- Bit-Level Scanline RLL Compression
- Direct Generic Printer Engine Interface to TEC, Canon, Ricoh and Oki Printer Engines
- Programmable ROM/EPROM
- Programmable DRAM Interface
- Register Programmable I/O Control
- Burst Interface Support for i960 KA/KB Processor Bus
- Automatic Data Conversion from 16-Bit Font Cartridge to 32-Bit i960 Embedded Processor Format



The Intel 82961KD Printer Coprocessor provides the Intel i960 KA or KB microprocessors with a powerful graphics accelerator and compression processor that dramatically increase system performance and reduces printer controller system cost. This single chip device provides all necessary system control for the i960 KA or KB microprocessors and a direct interface to most laser printer engines. The 82961KD controller contains complete DRAM, I/O and interleaved ROM controllers, font cartridge support and the associated logic required to control most non-impact printer mechanisms, a programmable wait-state generator and programmable chip select generation logic.

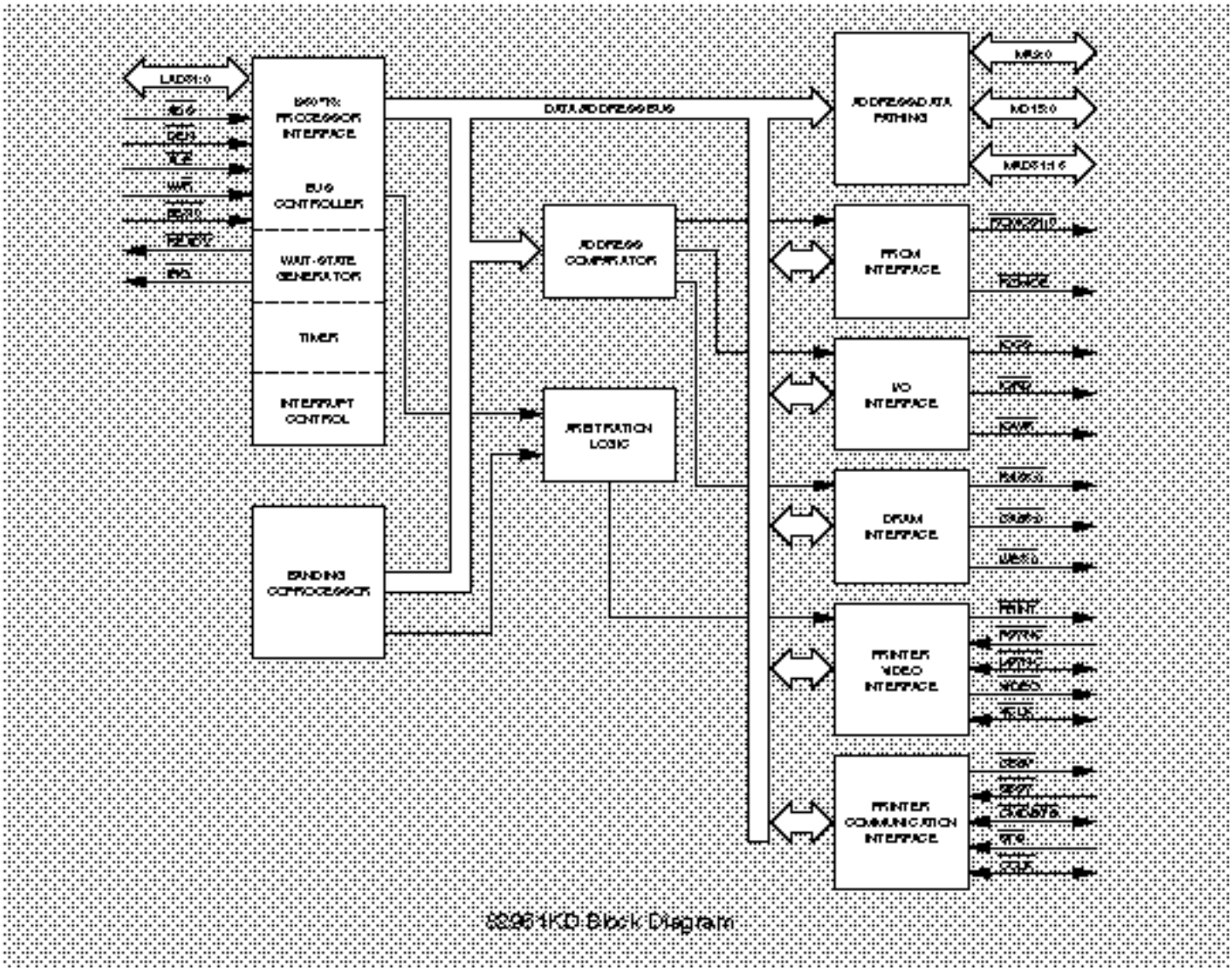
The 82961KD Graphics Coprocessor Printer Controller performs all graphics functions necessary for complex page description language (PDL) or printer control language (PCL) controllers. Image compression is achieved using "Scanline Tables." The memory required for storage of bit mapped images such as character font cache and graphics objects – is significantly reduced by using these structures.

The 82961KD controller processes a compressed display list to form the bit mapped image of the page to be printed. The 82961KD controller automatically supports "Band buffered" print operations. (The chip's compressed display list, coupled with its fast graphics operations and on-chip BitBLT accelerator allow band buffered printing of very complex page description language (PDL) pages such as those PostScript generates.)

PROCESSORS SUPPORTED:
i960 KA/KB Processors

AVAILABILITY:
Now for PQFP
164-lead at 16, 20, and 25 MHz in 2H94

CONTACT:
Local Intel Sales Office
WWW: http://www.intel.com/embedded/



80960KD Block Diagram