

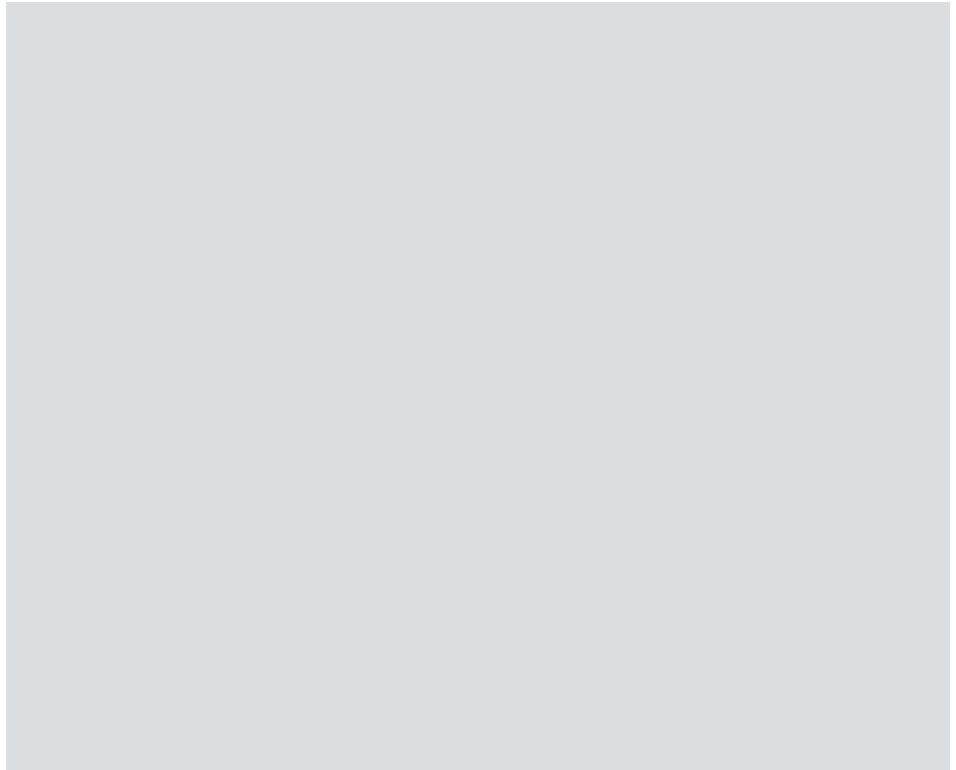
## XTK/TLC Cross Talk Tool Kit Transmission Line Calculator

- Complete PCB/MCM Transmission Line Simulation
- Simulate Signal Integrity Problems
- Calculate Pin-to-Pin Interconnect Delay
- Find Crosstalk Problems
- Back-Annotate to MOTIVE or Logic Simulator
- Complete System Analysis in Hours
- i960® Processor Models Included with Software

A high-performance, highly accurate signal integrity tool, Quad Designs XTK (Crosstalk Tool Kit) analyzes linear/non-linear effects, loss and crosstalk contributors; provides automated input of PCB wire topology; and features full timing back annotation to MOTIVE and digital simulators.

XTK is a powerful suite of network simulation/analysis tools consisting of XFX (Crosstalk Field Extractor) and XNS (Crosstalk Network Simulator). XTK calculates the electrical parameters of multiconductor configurations and simulates the inter-conductor coupling (noise) generated in complex digital systems. It can predict system level noise and transmission line signal integrity effects prior to costly prototype fabrication, ensuring fewer design iterations, improved time to market and higher quality designs.

Translators from all popular PCB layout tools are available including: Gerber Photoplot, Cadence Allegro, Calay V04 and Prisma, Cooper and Chyan Spectra, MentorGraphics Boardstation, SCI-CARDS, PADS PCB, Recal-Redac, VIEWlogic VIEWplace, Computer Vision.



### HOST SYSTEMS SUPPORTED:

XTK is available on most UNIX workstations as well as DOS platforms including: Sun, HP/Apollo, MIPS, SGI, DECStation, Solbourne, Data General, IBM RISC, IBM PC and compatibles.

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