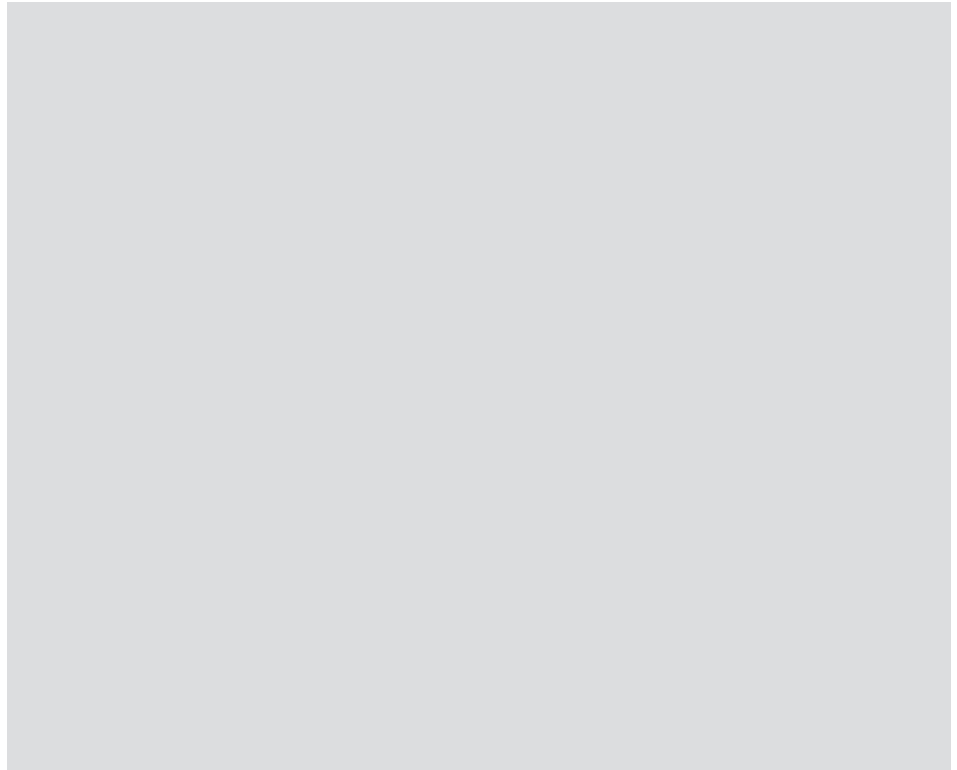


MULTI* Source Level Debugger



- Supports Programming in Ada, C, C++, Fortran, Pascal and Assembly Language
- Connects to Most Target Hardware Using Easily Adapable ROM Monitor
- Connects to Timing Accurate Instruction Set Simulator
- Connects to Popular RTOSs Including pSOS+ and VxWorks
- Host I/O Support Allows Target to Access Host Filesystem and I/O Devices Using Standard System Calls (e.g. *open()*, *read()*, etc.)
- Multi-Process Debugging
- Extended Symbol Information Supports *#defines* and *typedefs*
- A True C++ Debugger with Support for all C++ Constructs Including Templates and Multiple Inheritance
- Integrated Class and Object Browsers
- A True Ada Debugger w/Support for all Ada Constructs



The MULTI source level debugger supports programming in Ada, C, C++, Fortran, Pascal and assembly language. It is intuitive and easy to use; most programmers can get significant work done by simply invoking MULTI and following their instincts. MULTI works with compiled code, and is particularly suitable for workgroups developing very large programs.

The basic features of the debugger are illustrated in the diagram at right. A close inspection will show that almost all common debug operations have been reduced to a single mouse operation.

A unique debug server architecture allows MULTI to attach to and debug almost any hardware target. Most customers use the ROM monitor, which is easily configured to the user's hardware by adapting the I/O routines and linking appropriately. Green Hills has pre-ported the ROM monitor to many popular CPU boards; these ports are included in the distribution.

Native versions of MULTI are available for most UNIX workstations, so that you can use the same tools for native and embedded development. MULTI also supports most other 32- and 64-bit embedded microprocessors.

HOST SYSTEMS SUPPORTED:
PC/Windows, Sun/Solaris, Sun/SunOS, HP9000/HPUX, Alpha/OSF1, SGI/Irix, Aviiion/DGUX, PC/SCO, PC/UnixWare, RS6000/AIX, DECstation, more...

PROCESSORS SUPPORTED:
i960® Sx, Kx, Cx, Jx, Hx Processors

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For international contacts see Appendix B.

