Green Hills* Sim960 Instruction Set Simulator

- Simulates All Models of i960®
  Processors: Sx, Kx, Cx, Jx, Hx
- Instruction Cache Model Provides
  Measure of Locality of Program
- May be Controlled by Source Level
  Debuggers
- Fully Supported as a Target to
  MULTI* Source Level Debugger
- Handles Programs with Position
  Independent Code and Data
- Provides Execution Statistics for
  Profiling
- Target Program Can Do I/O on Host
  File System
- Supports Simulation of Floating
  Point Instructions

The Green Hills Sim960 instruction set simulator executes 960 programs on the host PC or workstation without the benefit of target hardware by simulating the execution of the target processor at the instruction level. A cache model tracks cache performance and provides usage statistics which may be used to alter the program to improve performance.

Sim960 incorporates an integrated debug server so that it may act as the target to a debugger. Programs can be loaded into memory, breakpoints may be set, the program may be started, halted and single stepped, and variables may be examined. When used with the MULTI debugger, the simulator can perform I/O using the host's filesystem and devices. Specifically, the *stdin*, *stdout* and *stderr* streams are routed to a window on the host. Standard system calls such as *open()*, *lseek()*, *read()* and *write()* can be used to manipulate files on the host, and shell commands may be issued using *system()*.

HOST SYSTEMS SUPPORTED:
PC/Windows, Sun/Solaris, Sun/SunOS, HP9000/HPUX, Alpha/OSF1, SGI/Irix, Aviion/DGUX, PC/SCO, PC/UnixWare, RS6000/AIX, DECstation, more...

PROCESSORS SUPPORTED:
i960 Sx, Kx, Cx, Jx, Hx Processors

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