

## SC3XXX Low Skew Clock Drivers

- Up to 20 Clock Outputs (Optional 10 Outputs)
- All Outputs Leading Edge Synchronized to  $\pm 500$  ps
- Source Series Terminated – No Additional On-Board Termination Required
- Support TTL Output Clock Rates to 80 MHz
- Accepts Either TTL or Differential PECL (+5 V Referenced ECL) Inputs
- Small Outline 52-Pin PQFP Package (Optional 28-Pin SOIC)

AMCC's SC3XXX family of clock drivers are specifically designed to distribute multiple clock signals. The drivers distribute up to 20 copies of a reference clock with low skew while effectively limiting induced ground noise. Use of a complementary (source and sink) 24 mA peak output driver limits leading edge skew among all outputs to  $\pm 500$  ps at frequencies up to 80 MHz.

These circuits provide "source (series) termination" designed to match the impedance of the most common printed circuit board (PCB) traces. The combination of the patented output driver with source termination effectively eliminates overshoot and undershoot without any additional on-board termination networks. The drivers are designed to support symmetrical rise/fall times of less than 1.5 nS without "ground bounce." AMCC has the broadest line of precision clock and timing products designed to meet the needs of your i960<sup>®</sup> microprocessor-based system.

Clock Driver Selection Guide				
Part	Output Frequency with Respect to Input Frequency			
	Total Outputs	Number of Outputs : 1	Number of Outputs : 2	Number of Selectable Outputs : 2 or 4
SC3500	4	10	1 + 1 + 2	2 + 2 + 4
SC3505	4	10	10	N/A
SC3507	20	10	N/A	10 + 10 + 4
SC3508	20	20	N/A	N/A
SC3517	10	5	N/A	5 + 2 + 4
SC3519	10	10	N/A	N/A
SC3524	5	5	4	N/A
SC3527	10	5	7	N/A
SC3529	10	10	N/A	N/A
SC3535	10	10	N/A	N/A
SC3538	14	8	8	N/A

\*All values are in 3V

PROCESSORS SUPPORTED:  
i960<sup>®</sup> Processor Family

AVAILABILITY:  
Production quantities available now

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