

S5933DK PCI Matchmaker Controllers Developer's Kit

- PCI 2.1 Compliant Kit Provides Full Access to PCI Configuration Registers
- Platform to Breadboard Initial Design Concepts
- Provides a Means to Start Software Development
- Tool to Familiarize Designers and Software Developers With the PCI Bus
- Supports 132 MB/Sec. Transfer Rate in Burst Mode
- Includes Software to Program the Configuration Registers and Non-Volatile BIOS Memory

The S5933DK PCI Controllers

Developer's Kit provides a proven reference platform for use in prototyping applications. The kit has a modular design that allows hardware "breadboarding" as well as general evaluation of the S5933Q PCI Matchmaker Controllers. Diagnostic and development utility software is included to help reduce time-to-market of both new and existing applications.

The S5933 demonstration board consists of a Revision 2.0 PCI Specification compliant edge connector and an S5933Q device packaged in a 160-pin plastic quad flat pack (PQFP). In addition, two pre-programmed 22V10 Programmable Logic Devices (PLD's) are provided for use in pass-thru mode. Both serial and byte-wide Flash EPROMs are included and may be programmed by the user.

Via on-board connectors, the S5933 board can be interfaced to either of two breadboard daughter cards that are included in the kit. Each daughter card has a PLD socket along with 3 x 4 inches of uncommitted wire wrap area and 2 x 3 inches of customizable wire wrap area.

Alternatively, the evaluation board can be interfaced with the ISA loopback card provided. The ISA card consists of a standard edge connector and a ribbon cable which connects to the S5933 evaluation board.



The ISA card allows the user to "emulate" an add-on processor using either the host system or a second PC. The loopback feature provides direct access to the S5933's add-on bus, enabling the user to pass commands and data through the S5933 on to the PCI bus. The uncommitted breadboard area provided by the ISA card allows for customized functionality.

Software developers' tools included with the kit allow the user to exercise various configurations and controller registers using a "high level" DOS interface, AMCC DIAG, a diagnostic program which also verifies the integrity of the S5933 data. The NV Build program assists in the in-system programming of the external non-volatile memory to implement application-specific configurations.

A user's manual and full design documentation complete the kit.

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