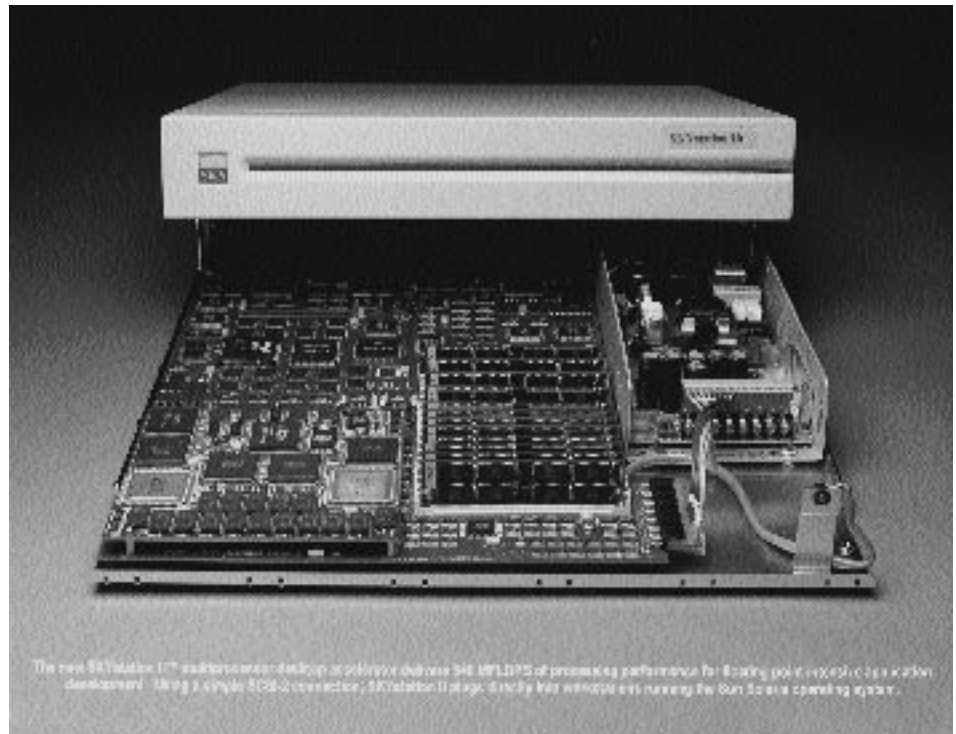# SKYstation II*

- 640 MFLOPS Plugged Directly into SCSI Port
- i960® Processor Handles Interrupts and I/O Leaving the i860™ Processors Free For Processing
- 512 Mb Bulk Memory and 128 Mb Local Memory
- SKY Parallel Compute Cluster Toolkit Enables Four i860 Processors to be Programmed as a Single Processor
- SKYchannel Packet Bus Delivers I/O at 320 Mb/sec.
- SKYvec Software Development Environment Speeds Development Time
- SKYvec Includes Automatic Optimizing, Parallelizing and Vectorizing C, FORTRAN and Ada Compilers
- Barrier Registers Provide Processor Synchronization With No Impact to Memory
- Synchronization is Handled Transparently; Simply Compile and Execute Your Program
- Achieves 80 Mb/Sec. Communication Without Tying Up the Host Bus

The new SKYstation II™ multiprocessor desktop accelerator delivers 640 MFLOPS of processing performance for floating point, vector, and application development. Using a simple SCSI-2 connection, SKYstation II plugs directly into workstations running the Sun Solaris operating system.

The SKYstation II* provides 640 MFLOPS of compute power. With a simple recompile, your software will be accelerated, in parallel, thanks to SKY Computers' exclusive Parallel Compute Cluster Architecture*. The compilers in the SKYvec* Software Development Environment automatically optimize, vectorize, and parallelize your software. SKYvec transparently runs your application on four processors as easily as a single processor, or on eight processors as easily as two – with a near linear speedup.

The SKYstation II uses an i960 super-scalar processor chip located on the motherboard to manage all I/O tasks, freeing the i860 processors for their role as compute processors. The i960 processor is optimized to handle these operating system and I/O functions, and the compute

power of the i860 processors is focused on solving the application problem.

SKY adds a 256 Kb SRAM used as zero-wait-state RAM and a 512 Kb Flash RAM for non-volatile storage of configurable boot code. This configuration makes the i960 processor ideal for system-level tasks. It quickly fields all interrupts and informs an i860 processor only when there is a task for that i860 processor. This quick interrupt response enables effective scheduling of the SKYmpx real-time executive. The bulk of the operating system is provided by the i960 processor, freeing the i860 processors to run more efficiently with a small kernel.

HOST SYSTEMS SUPPORTED:
SCSI-based computing system

PROCESSORS SUPPORTED:
i960 CA Processor

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SKY