

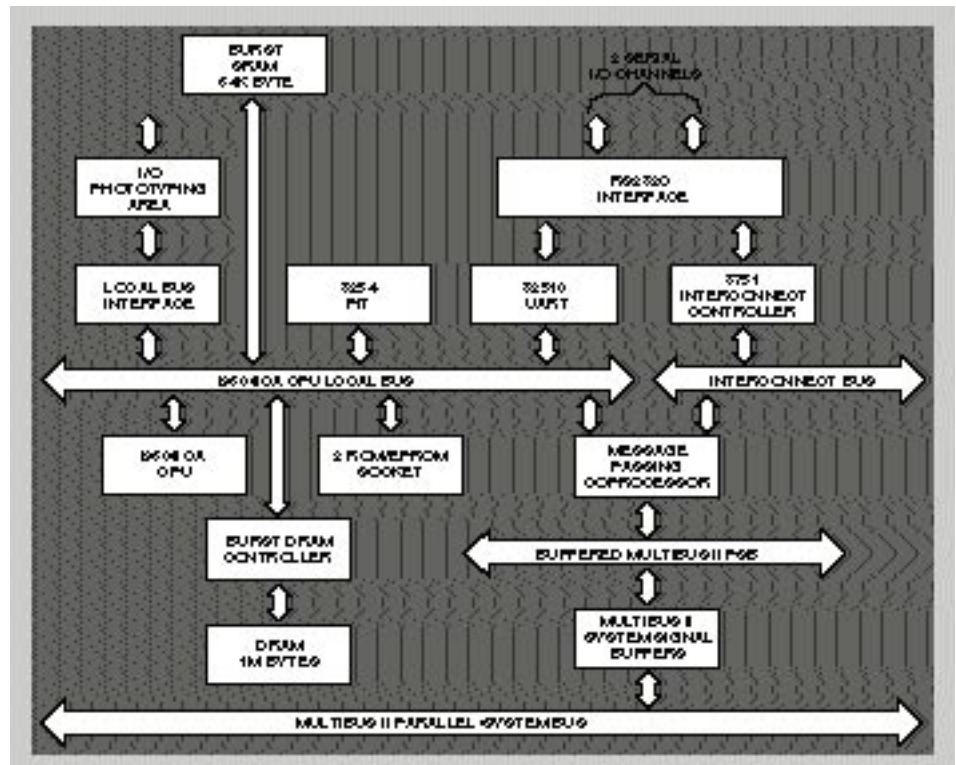
MIB II 960/110A RISC Development Board

- IEEE-1296 Multibus II Interface
- Full Message-Passing Capability
- i960® CA Processor at 33 MHz
- One or Four Mbyte of DRAM
- One Socket For Up to 512 Kbytes of ROM/EPROM
- I/O Prototyping Area
- 82510 UART
- Interconnect Support For the PSB
- Four DMA Channels
- 8254 Programmable Interval Timer
- Support Software For Intel i960 Processor Monitor, iRMK*, Transport Protocol and BIST

The MIB II 960/110A RISC Development Board from Micro Industries is a RISC processor board based on Intel's i960 CA processor that is capable of utilizing the 40 Mbyte/sec. Multibus II bandwidth better than any other product today.

The basic board consists of an i960 CA processor, an independent configuration processor, a message-passing coprocessor, one socket for up to 512 Kbytes of EPROM memory, 1 or 4 Mbyte of DRAM, an 8254 programmable interval timer, and an 82510 UART serial I/O channel. The board also provides an open prototyping area with an interface to the 32-bit local processor bus with interrupts, DMA control lines and wait state generation circuitry. Support software includes a monitor/debugger, message transport service, interconnect firmware and BIST diagnostics.

As a development platform, this highly integrated board is particularly suited for FDDI, Ethernet, MAPS, and Token Ring, as well as a variety of custom high-speed applications. It enables designers to design new systems more quickly, minimize errors, and ultimately save money. In addition, the software provided with the board reduces debugging and other software development costs and allows OEMs to concentrate on their own application software.



PROCESSORS SUPPORTED:
i960 CA Processor

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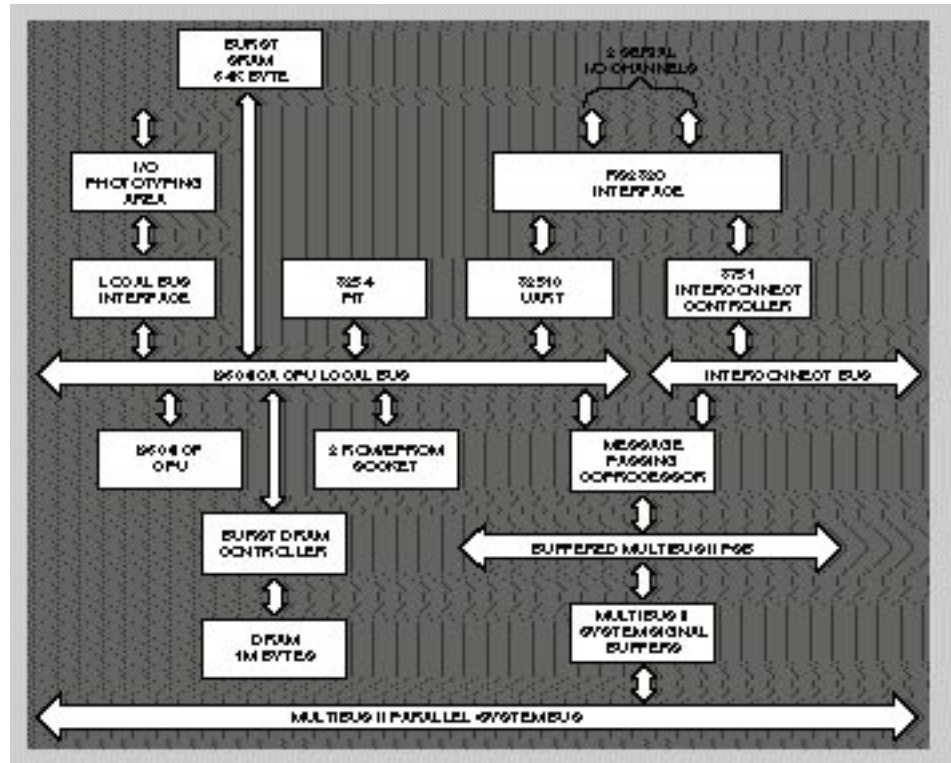
MIB II 960/110B RISC Development Board

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