

TMax* 5401 i960® Hx Processor In-Circuit Emulator



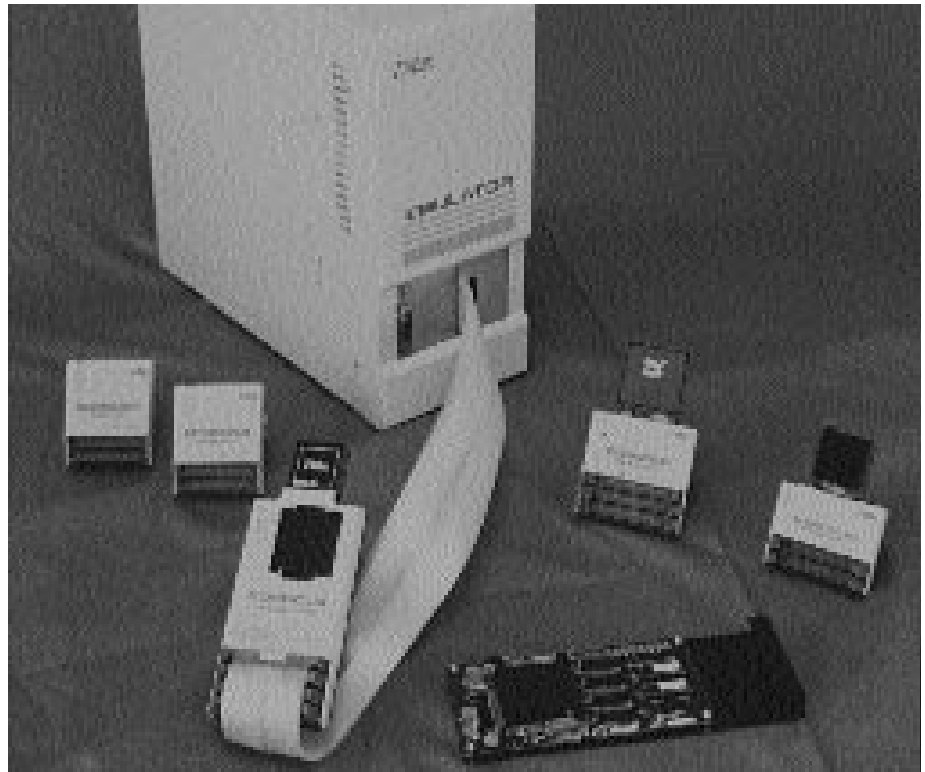
- Full Trace and Event Detection Within Cache
- Full Support For Clock Multiplied Processors
- Operation With Low Power/3.3V i960® Processors
- Graphical User Interfaces
- Performance Tuning Tools - VPTools
- Special Imaging Support Features
- SW and HW Assisted Software Profiling
- User-Installable Emulator Options
- Modular Design Lowers Overall Tools Cost

Topmax offers the TMax* 5401 In-Circuit Emulator for the Intel i960 H-Series of microprocessors. The TMax 5401 emulator is the logical extension of Topmax's C-Series emulators. It complements the existing TMax 5001/2 CA/CF-Series emulators products. The Hx emulator is a powerful tool for HW-SW development and for product test and support.

The TMax 5401 is a full scale, real-time debugging environment for the i960 Hx microprocessor series. The emulator supports full speed cache execution with instruction cache reconstruction and a flexible, deep and upgradable real-time trace memory system coupled with a powerful event-triggering capability. This lets users take full control of any debugging situation.

Users can view code using the supplied source level debugger in C source or in mixed formats on a workstation or PC host. You can view an on-screen logic analyzer display or use VPTools, a set of software and hardware performance measurement and optimization tools, for system tuning. Developers who work with bit-map images can view them using a unique bit-image display useful for imaging and printer applications.

Use the supplied multi-windowed user interface with symbolic source level



debugging or add optional debugging environments based on GDB960 or Microtec Research, Inc.'s XRAY Debugger. Other options include 4-8 MB emulation memory, a logic analyzer, workstation support, i960 processor upgrade kits (Sx, Kx, Jx, RP, Cx) and additional trace memory depth (to 32K cycles).

HOST SYSTEMS SUPPORTED:
Sun SPARC, HP9000/700, IBM PC/AT

PROCESSORS SUPPORTED:
i960 Hx Processor

AVAILABILITY:
Q4 '95

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