EL 3200 Emulator for i960® CA/CF Processors
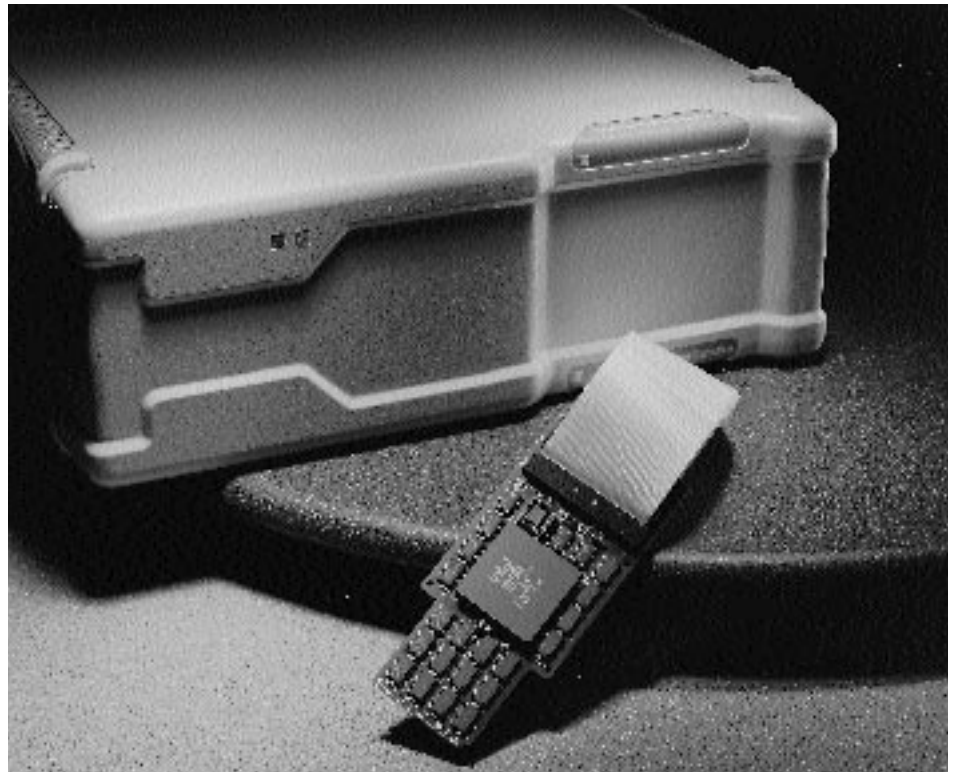
- Real-Time, Transparent Emulation at 40 MHz
- Support For Pipeline, Burst Mode and All i960® CA/CF Processor Bus Sizes
- Integrated C/C++ Source-Level Debugger
- Compatible With Intel, GNU, and MRI Compilers
- Can Disassemble Trace When Executing Out of Cache
- High-Speed Parallel and Seamless Ethernet TCP/IP
- 16K x 139 Bit-Wide Trace and Up to 4 Mbyte of Overlay Memory
- Dynamic Access to Emulator Functions While Running the Target System
- Profile Support For Optimizing Compilers
- Big-Endian Support
- Fully-Buffered Probe Tip For Maximum Target Safety

With the EL 3200 engineers can quickly find and fix both software and hardware problems in embedded systems. The EL 3200 supports the unique features of the i960 CA and CF processors, such as instruction caching, pipeline, and burst modes as well as different bus widths.

Internal register configurations can be mapped to provide quick access to the states and meanings of the bit fields comprising the MCONs, PRCB, and IBR. The emulator software automates gathering and formatting of run-time data to make profile-based optimization a snap.

The EL 3200 runs on Sun or HP workstations as well as PCs. The emulator attaches to any Ethernet network and can be accessed from any workstation or PC on the network. The EL 3200 also offers both an integrated source-level and a symbolic-level debugger. The MWX-ICE source-level debugger supports interleaved display of source and assembly listings. The menu driven, easy to use multi-windowed interface also provides the option of a line-oriented command interface for functions such as automatic setup and testing, session recording, and command macros.

The EL 3200 is equipped with a sophisticated trace and event system that enables the tracking and isolation of complex real-time interactions between hardware and software. An available 16-bit logic state probe adds logic analysis capabilities. In addition, the EL 3200 offers 16 hardware access breakpoints to break on a read or write to a data location. The emulator's 16 real-time hardware execution breakpoints, over 1,000 software execution breakpoints, and the multiple event system breakpoints help engineers locate bugs quickly.

HOST SYSTEMS SUPPORTED:
PC, Sun SPARC, HP 9000/700

PROCESSORS SUPPORTED:
i960 CA/CF Processors

AVAILABILITY:
Now

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