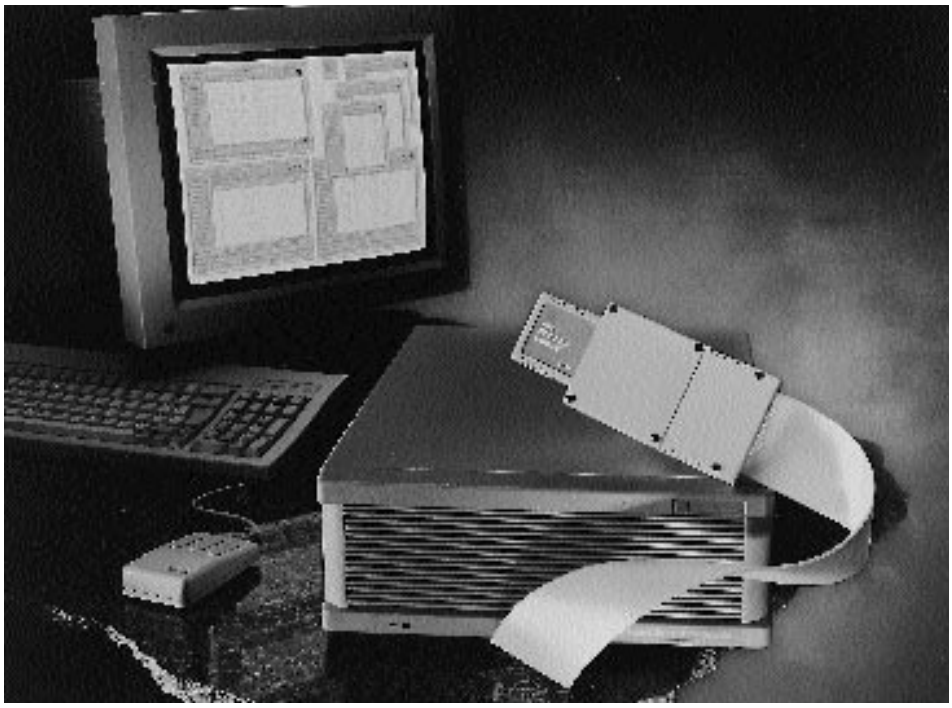
CodeICE* 960 for i960® Jx Processor Series

- Supports i960® Jx Processor Series With One Probe and Debugger
- Real-Time Emulation at Full Processor Speed With Designed-In Support For Higher Speeds
- 32K x 128-Bit Trace Buffer With Unique Source-Level Tracing and Trace With Caches Enabled
- Up to 8 MB of Overlay Memory
- 26 Hardware Access, 18 Hardware Execution, and Thousands of Software Breakpoints Work With Powerful, Easy-to-Use Event System
- Performance-Plus Tools For Enhancing and Optimizing Product Performance
- CPU Browser*: Graphical Internal Register Configuration and Control Tool
- Seamless Networking Support For Workstations and PCs
- Supports Intel, MRI and GNU Compilers



The CodeICE* 960 Jx emulator delivers affordable, no-compromise performance – such as 50 MHz internal and 40 MHz external bus speeds and a rich feature set that supports the entire J-series with a single probe and debugger. Applied's CodeICE emulators are also designed to broaden the traditional range of emulator activity to span the development cycle from concept through maintenance and support.

The CodeICE includes support for the i960 processor-specific features such as Big-Endian byte ordering, caching, different access modes and bus widths. The emulator operates from a Sun SPARC or HP workstation, and from a PC-hosted network. It can also be accessed from a PC through a high-speed parallel interface. By focusing on processor architecture, CodeICE 960 Jx delivers unmatched productivity enhancing capabilities such as CPU Browser*, a graphical interface for configuration, display and modification of the states of internal processor registers; IDP/Dynamic-Mode, for servicing target interrupts while emulation is paused and dynamic access of emulator features without stopping emulation; RTOS-Link*, for integrating support of industry-leading real-time operating systems with CodeICE; and Performance Plus tools, offering convenient profiling and code coverage support for use with Intel's optimizing C compilers, integrated performance analysis and programmable timestamping. Applied's unique source-level tracing capability displays which source lines were executed and correlated values while executing out of cache.

Included with CodeICE 960 Jx is the MWX-ICE C/C++ debugger environment. MWX-ICE lets you graphically organize your approach to development so you can work more easily and efficiently. You can display source code, synchronized bus-level and disassembled trace, processor registers and other information in a multi-windowed environment customized to suit you. MWX-ICE and CodeICE can run standalone or integrated with the MasterWorks environment from Microtec Research.

HOST SYSTEMS SUPPORTED:
PC, Sun SPARC, HP 9000/700

PROCESSORS SUPPORTED:
i960 JA, JF, JD, 80L960JA, 80L960JF Processors

AVAILABILITY:
Now

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