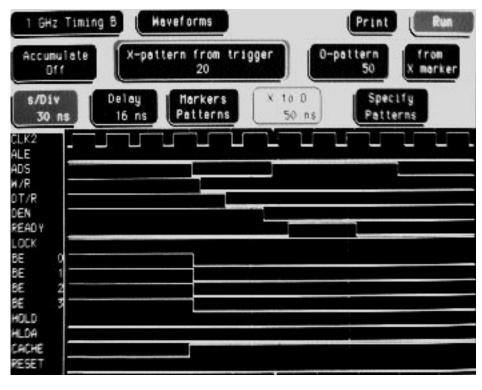
HP 16500B Logic Analysis System



- All the Same Features as the HP 1660 Analyzers Plus Modularity, Color Touchscreen Interface and LAN
- Configure Up to 204 Channels on the Same Time Base For 500 MHz Timing/110 MHz State
- Display Executed Code Easily With Symbolic References From Your Language System Using the HP E2450A Symbol Download Utility
- Move Data to Your Workstation or PC With the HP16500L Ethernet LAN Interface Card or Store Files Locally on the Internal Hard Drive
- Replace Missing System
 Components With the HP 16522A
 Pattern Generation Module
- Grow Beyond the Five Modular Slots in an HP 16500B With an HP 16501A Expander Frame
- Display Code Flow in High Level Source Code on Your PC or Workstation Concurrent With Microprocessor Mnemonics

When used for Intel i960® processor development, the HP 16500B Logic Analysis Mainframe provides the i960 processor developer with a range of solutions. The HP 16555A is a 68-204-channel logic analyzer supporting 110 MHz state and 500 MHz timing analysis. If greater resolution is needed for timing analysis, or parametric measurements are needed, the HP 16517A 4 Gigasample/sec Timing analyzer and the HP 16534A 2 Gigasample/sec Oscilloscope are available. In the HP 16500B system, any module can trigger or be triggered by any other module.

The HP E2432A Intel i960 CA/CF preprocessor interface allows you to easily connect the logic analyzer to your i960 CA/CF processor-based target system. The preprocessor keeps track of the i960 CA/CF processor address pipeline. When state traces are displayed, data is aligned with the correct address.



PROCESSORS SUPPORTED: i960 Sx/Kx/Jx/Cx/Hx, MC Processors

AVAILABILITY:

i960 KA/KB/MC preprocessors available from Corelis, Inc.

Phone: (310) 926-6727

CONTACT:

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Bldg. 51L-SC

Santa Clara, CA 95052-8054

Phone: (800) 452-4844

For international contacts see Appendix B.

