

## ML 4400/TGI-960 Logic Analyzer and Disassembler



- Full Disassembly Support for i960® Microprocessors
- Supports i960 Processor Speeds at Maximum Frequencies
- Symbolic Debug in Disassembly
- Time Stamp and Performance Analysis
- Multiple Analyzers in Same Four Slot Chassis
- Cross-Trigger Timing Analysis to 1 GHz With i960 Family Support
- Capture Up to 128 Thousand Events on the i960 Processor

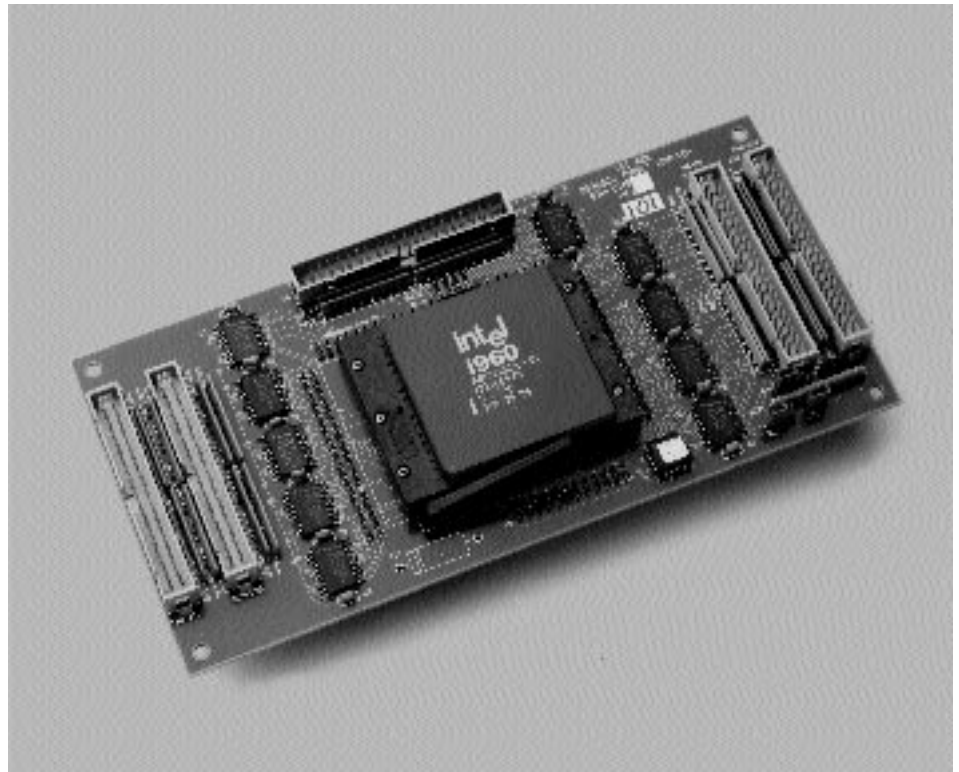
American Arium's i960 microprocessor support package consists of a target interface adaptor (TGI-960) and i960 processor disassembler. This hardware and software package, when used with the ML4400 logic analyzer, provides the mechanical interconnections as well as full software disassembly.

The target interface adaptor plugs directly into the PGA socket for the i960 processor on the board under test. The processor is then plugged into the adaptor. A zero-insertion-force (ZIF) socket provides for easy installation and removal of the CPU.

The TGI-960 provides buffering on all signals, and very-low-inductance connections from the processor down to the socket. This gives both very dependable i960 processor instrumentation and excellent target isolation.

The i960 processor software disassembly is implemented with an Arium proprietary disassembly tool, the user-defined disassembler (UDD-001). This disassembler supports all instructions, and its display shows each data cycle.

Arium's i960 processor target interface adaptor is an excellent example of how the ML4400's synchronous channel bandwidth (up to 400 channels at 100 MHz) meets the demanding requirements of today's RISC architectures.



Another 100 MHz logic pod can be added to the system to monitor more processor signals. Four of these signals are user-jumperable, providing flexibility in hardware debug situations.

PROCESSORS SUPPORTED:  
i960 Processor Family

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