

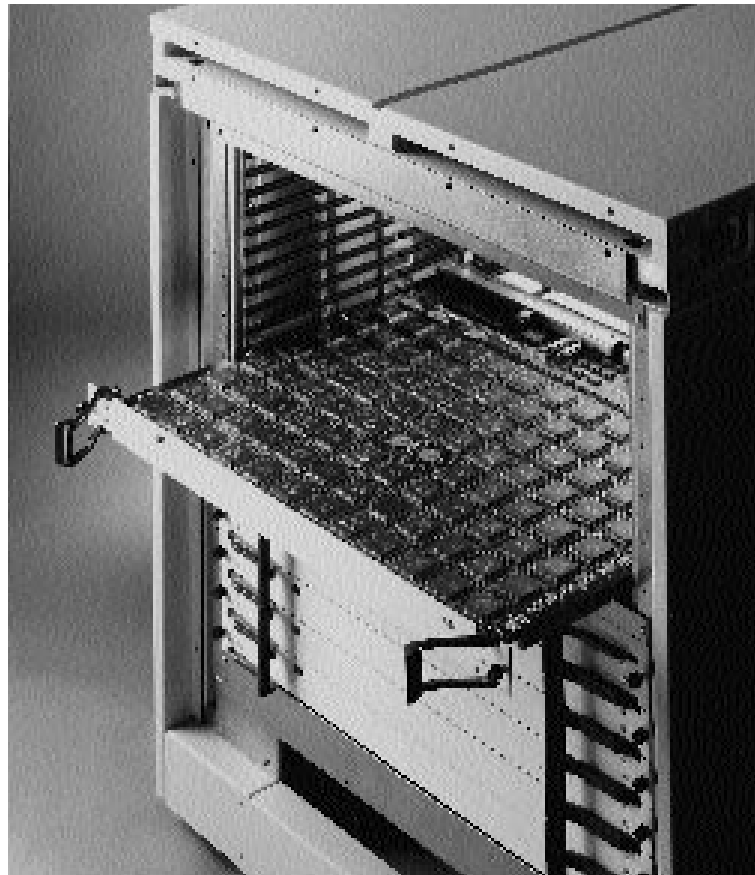
System Realizer* Emulation System for System Level Verification

- Validate Software and Complete System Design Before Fabricating ICs
- Reduce Risk of Product Delay by Verifying ASIC/ICs in the System Environment Before Silicon Fabrication
- Save Months on Overall Time-to-Production Through Early System Integration
- Verify Post Silicon Changes With Pre-Verified Debug Environments

The System Realizer* Emulation System from Quickturn Design Systems, Inc. is a design tool to enable real-time, system-level design verification through in-circuit emulation and to assure that ICs and ASICs work the first time.

The System Realizer Emulation System allows rapid prototyping of ASIC/ICs directly from all standard EDA netlist formats. The Quest software reads the design netlist and creates a "virtual silicon" functional prototype by mapping designs onto an array of FPGAs. A System Realizer option, HDL-ICE* further reduces time-to-emulation by providing direct Verilog RTL mapping to the emulator. The emulation system can be plugged into a target system in place of the ASIC/ICs yet to be fabricated. This creates a complete system and allows software validation and full system verification months ahead of when it is otherwise possible. Built in debug tools allow users to find bugs and correct them quickly using incremental configurations. A single System Realizer system can emulate up to 3M gates. The built-in logic analyzer helps debug the design as it is plugged into the target system.

The System Realizer system allows rapid prototyping of customer specific IC designs based on the i960® processor. Users can create a functional equivalent of an i960 processor-based design by placing



the i960 chip on a System Realizer component adapter card and configuring the rest of the IC logic in the System Realizer's FPGAs. Software validation and system integration can start ahead of first silicon being available. For systems using ASICs in conjunction with packaged i960 processor parts, either the System Realizer M250, M3000 or Quickturn's Logic Animator* emulation system may be used for ASIC prototyping.

PROCESSORS SUPPORTED:
i960 Processor Family

AVAILABILITY:
Now

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