

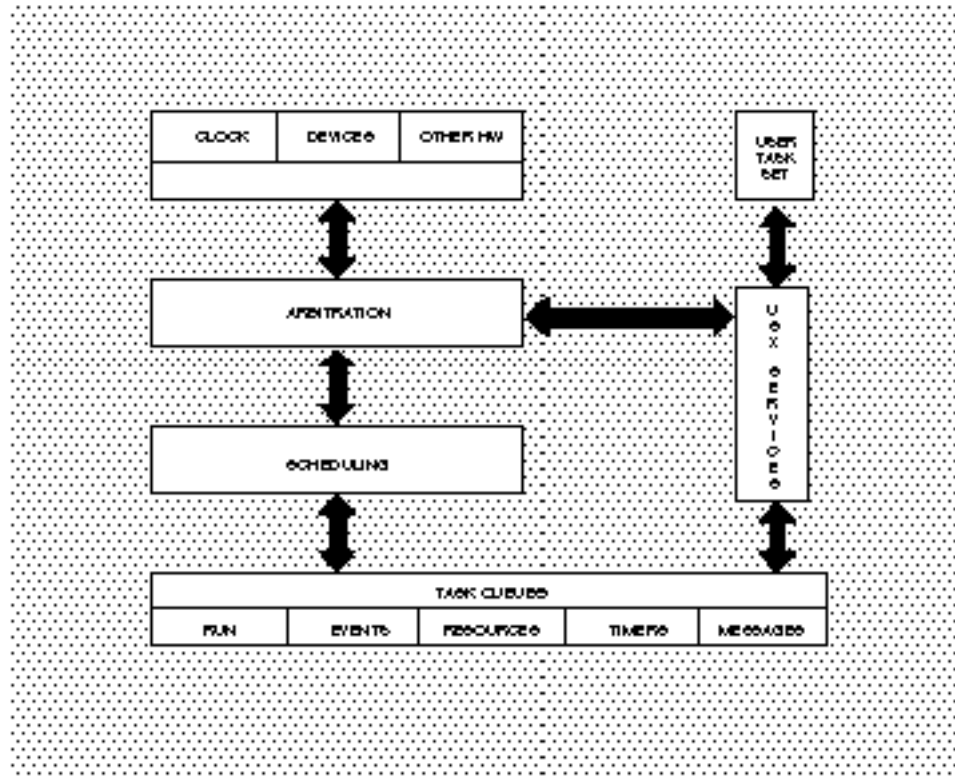
SuperTask! Kernel Real-Time Executive

- ROMable and Reentrant
- User Configurable
- Full Source Code in ANSI C
- Both Preemptive Scheduling and Time Slicing
- Dynamic Task Management
- Resource Management
- Event Management
- Inter-Task Communication
- Dynamic Memory Management
- User's Interrupt Handlers Can Stimulate Task Switching
- Very Low Interrupt Latency
- Conformance Test, and Debugger Task Included
- Reentrant Stream I/O ANSI C Source Library

US Software has introduced i960[®] processor MultiTask!* Kernel, a real-time executive designed specifically for Intel's i960 processor series. The product features a fast, compact executive (7 Kbyte object) with product delivery in source format. The Multi-Task! executive provides preemptive, priority-based task scheduling to the C user as well as a comprehensive set of system services accessible through U.S. Software's standard interface.

The MultiTask! solution is a software environment that runs both simple and complex tasks simultaneously in an embedded i960 processor application. The MultiTask! kernel provides mechanisms to ease start-up, manage tasks, support inter-task communication, manage real-time events, synchronize activities, arbitrate access to resources, profile tasks and perform other real-time related services.

The MultiTask! series is a user-configurable family of multi-tasking executives designed specifically for embedded real-time applications on microcontrollers/microprocessors. To enhance portability and implementation of control applications, the MultiTask! kernel provides a common interface to a comprehensive set of system functions across various microcontrollers/microprocessors. This allows



application development and prototyping and debug to proceed both on the i960 processor target as well as the host system.

For example, if an IBM PC compatible is used as a host, the 8086 MultiTask! product could be used for development, prototyping and debug. In this scenario, much of the non-hardware specific prototyping and debugging could be done using a host debug tool such as Microsoft's Codeview or Turbo Debugger. Target level debugging would then be done using standard cross development tools in conjunction with the MultiTask! debug task.

The code size of the i960 processor MultiTask! is less than 10 Kbytes. The product is delivered in source form, together with documentation, examples and a comprehensive test suite. The product has been exercised extensively with both Intel's iC-960 and GNU960 C compilers. A demonstration delivery is available.

PROCESSORS SUPPORTED:
i960 Processor Family

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