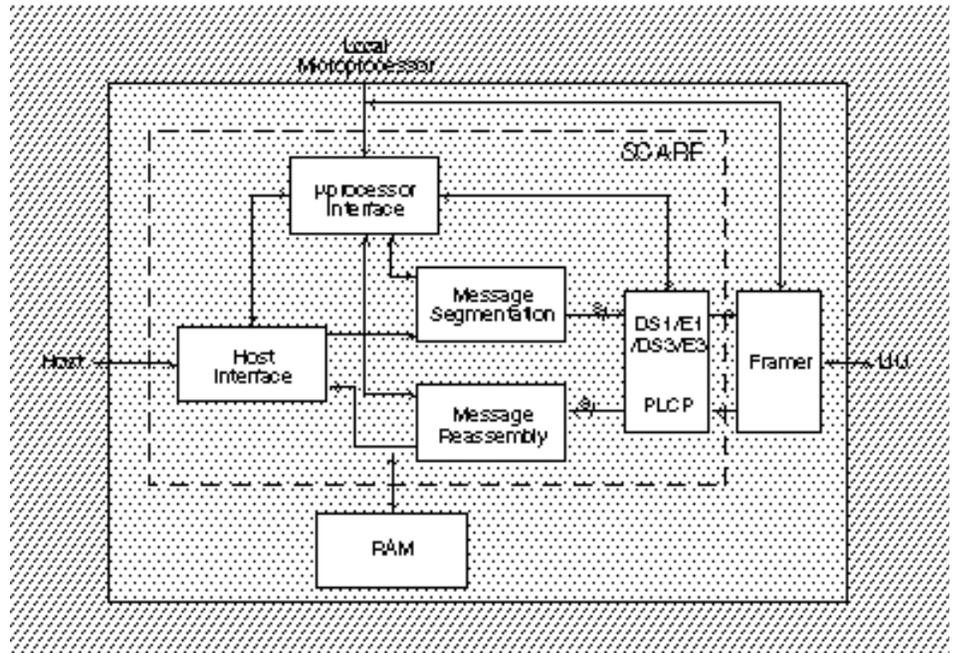


Bt8210 SMDS Control and Reassembly Formatter (SCARF)

- Complete SMDS Subscriber Network Interface (SNI) Implementation
- Operates at DS1, E1, DS3, and E3
- Compliant With Bellcore and ETSI Standards
- Switch and CPE Features
- Support For EISA Bus
- Includes Address Screening and Credit Manager Functions
- Full Segmentation and Reassembly Capability
- Directly Interfaces With Brooktree Framers
- Full DXI Support (According to SIG TS/001 1991)
- Evaluation Board Available



The Bt8210 SMDS Control and Reassembly Formatter (SCARF), is a single chip CMOS IC implementation of the SMDS Subscriber Network Interface (SNI). It is designed to be fully compliant with Bellcore TR-TSV-000772, TR-TSV000773, TA-TSY-000774, and TR-TSV-000775, ETSI Standards ETS 300 213 and prETS 300 214, SIG TS-001/91, and to support DS1, E1, DS3 and E3 SNIs.

The SCARF can be used in either a network switch or in CPE Equipment, and includes all HOB A features necessary to fully implement an SMDS SNI from the switch side. SCARF provides full segmentation and reassembly capability, and can reassemble 128 concurrent L3_PDU's. The SCARF offers full access to the protocol at all levels, and gives octet-wide simultaneous transmit and receive access to the full PLCP (57 octets), and L2_PDU (53 octets), the segment (48 octets) and the segment payload (44 octets). The SCARF provides real-time address screening, both for ingress and egress, as required in TR-TSV-000772, and also implements the "credit manager" function.

SCARF's host interface enables 16-bit or 32-bit wide connection to EISA and VME

bus interface devices, as well as a "clocked octet" interface, capable of burst transfer rates of up to 33 Mbyte/sec. The local processor interface is designed to support the Intel i960® processor family as well as other popular processors, and the local processor can be used as a host interface as well.

PROCESSORS SUPPORTED:
i960 Processor Family

HOST SYSTEMS SUPPORTED:
The Bt8210 supports EISA and a generic octet-wide host interface.

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