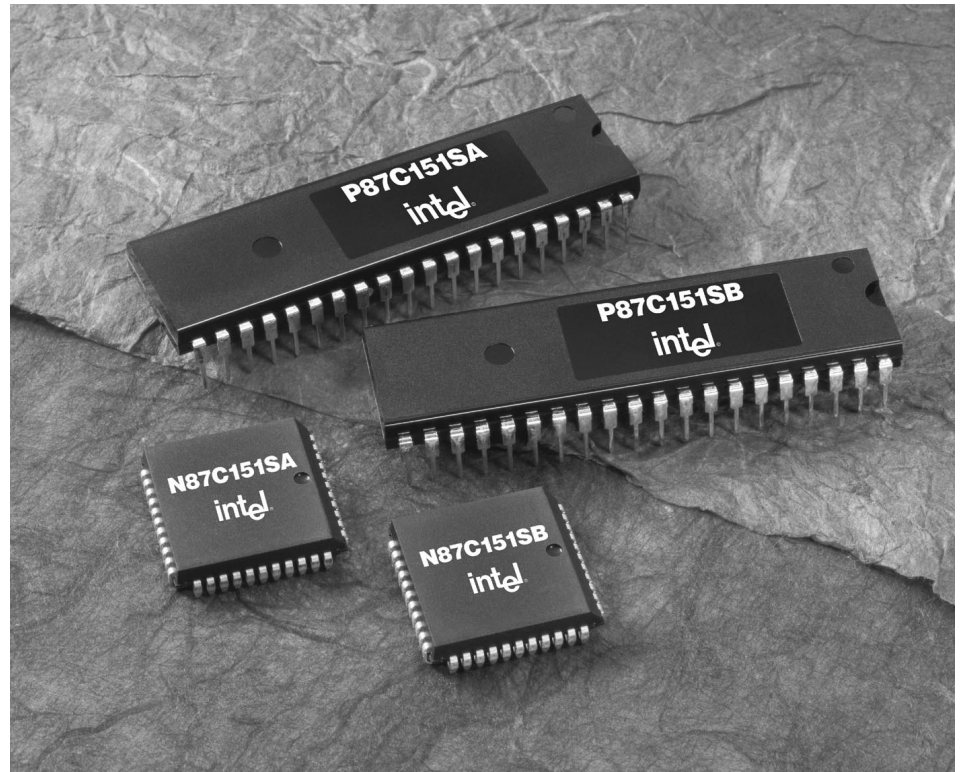


## MCS<sup>®</sup> 151 Microcontroller

- Pipeline Instruction Execution Unit
- MCS<sup>®</sup> 51 Microcontroller Compatible Instruction Set
- 16-Bit Internal Code Bus
- Page Mode and Wait States Configuration Options
- 8-Bit, 2-Clock External Code Fetch in Page Mode
- User-Selectable Configurations:
  - External Wait States (0-3 Wait States)
  - Page Mode
- Up to Five Times Performance Increase Over MCS 51 Microcontroller
- Hardware and Software Investment Protected
- Low RFI
- Quick Time to Market

The 8XC151SA/SB is instruction set compatible with MCS 51 microcontroller. It comes in 40-lead PDIP and 44-lead PLCC maintaining pin compatibility with MCS 51 microcontrollers. The 8XC151SA/SB has 256 bytes of on-chip RAM and is available with 8/16 Kbytes of on-chip ROM/OTPROM or ROMless. Several new features like programmable wait states, page mode and Extended ALE can be selected by using the new user programmable configuration. Key features like Instruction pipeline, minimal of 2 clocks per instruction and 8-Bit 2 clock external code fetch in page mode are available.



PRODUCT FAMILY:  
8xC151SA, 8xC151SB

DEVELOPMENT PLATFORMS:  
MCS<sup>®</sup> 151 Microcontroller  
Evaluation Kit

ENVIRONMENT:  
PC (Windows)

AVAILABILITY:  
Now

CONTACT:  
Local Intel Sales Office  
WWW: <http://www.intel.com>