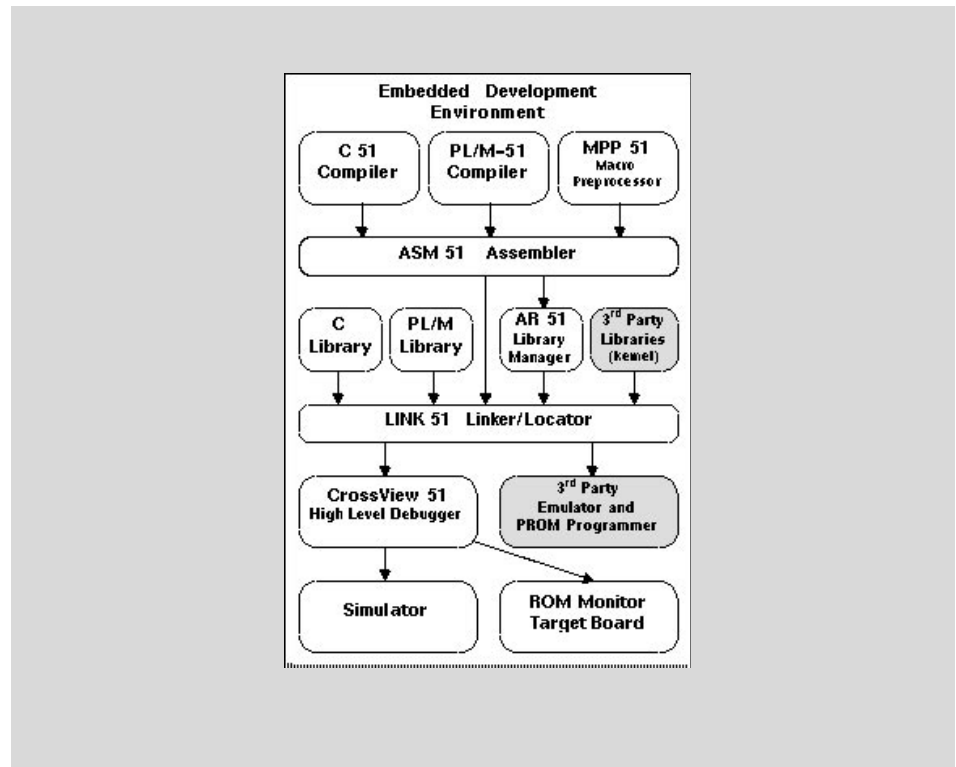


TASKING PL/M-51 Compiler

- Windows* Based Embedded Development Environment
- PL/M Listfile For Debugging With Third-Party ICE's
- Intel Compatible Optimization Levels (0-4)
- Interrupt Functions in PL/M and In-Line Assembly
- Calling Interface With C 51 and ASM51
- Generates Intel Compatible Assembly
- Intel Compatible Utility Library
- Full Intel OMF-51, IEEE-695 and Intel Hex

The compiler package consists of the PL/M-51 compiler, macro assembler, linker/locator, libraries, CrossView 51 simulator and EDE our Windows based embedded development environment. PL/M-51 is a structured high level language defined by Intel. The TASKING PL/M-51 compiler is compatible with both Intel's language definition and TASKING's other 8051 tools. Features and facilities in the PL/M-51 compiler make code production very efficient while ensuring portability between C and PL/M. This enables users to continue with their PL/M investment and have the opportunity to move to the C language. The PL/M compiler supports a full calling interface with parameter passing to our C 51 compiler and ASM 51 assembler.

You can assign each PL/M variable explicitly to a specific memory type: main, idata, register, auxiliary, constant. The compiler supports 1 and 2 byte pointers using based variables and data can be stored at fixed locations using at. The language allows fast 8-bit arithmetic, including true byte and bit type expressions. The compiler allows in-line expansion of pre-defined functions such as shl, shr, rol, ror, scl, scr, testclear, and time. Intermodule data overlaying by the linker stretches the utilization of the scarce internal RAM. The compiler is delivered with PL/M



libraries, run time libraries and an Intel compatible utility library. Third-party support includes interoperability with emulators (such as Ashling, Ceibo, Checkmate, ComAp, HP, Kontron, Lauterbach, Metalink, Nohau, Orion Instruments, Philips, Signum Systems, Sophia Systems) and other tools (ChipTools ChipView51, Intel *Ap*BUILDER).

MICROCONTROLLERS SUPPORTED:

8xC3x, 8xC5x,
8xC51Fx/Rx/Gx/SLAH/SLAL, 8xL5x,
8xL51Fx/Rx/Gx/Kx/SLAH/SLAL

DEVELOPMENT PLATFORMS:
Windows 3.1, Windows 95, Windows NT,
Extended DOS, Sun SPARC/SunOS, Sun
SPARC/Solaris, HP9000, DEC Alpha,
VAX/VMS

AVAILABILITY:

Now

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