

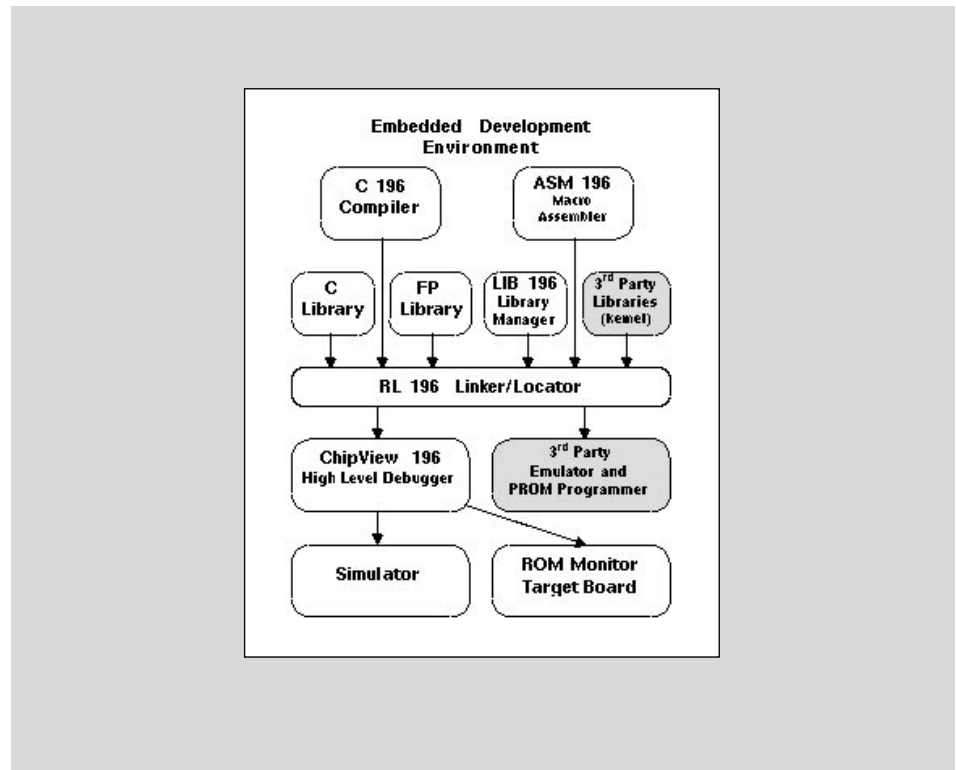
ChipView 196 ROM Monitor Debugger

- Supports C, PL/M and Assembler
- Full Support For 16- and 24-bit MCS[®] 96 Microcontrollers
- Interfaces to Any Intel MCS 96 Microcontroller DEMO or Eval Board
- Conditional Code and Software Data Breakpoints
- I/O Simulation Using Files, Timed or Polled
- Full Trace Including C, Assembler and Stack
- Data Monitoring of Expressions and Variables
- Includes Assembler Source of User Customizable RISM
- Context Sensitive Help and On-Line User Manuals

The ChipView Windows debugger is a true Windows* application and makes full use of Windows controls such as toolbars, tooltips and context sensitive pop-up menus. TASKING is a distributor of ChipView-196.

The RISM based ROM Monitor version of ChipView-196 interfaces directly to any Intel evaluation or demo board running RISM for the MCS 96 microcontroller family. Assembly source of the RISM kernel is supplied to permit user customized implementations. ChipView is the High-Level Debugger replacement for Intel's ECM host software and can run at speeds up to 115 Kbaud.

With ChipView there are plenty of views to choose from: source level code, assembly level code, watches, inspectors, on-chip registers, C call stack, variables, breakpoints, execution trace, session log, elapsed cycles, memory dumps and more. See all of your variables (global and local) at once in the variables window, or just track a few expressions in the watch window. Decompose arrays or structures across several inspector windows. With ChipView-196 you simply point and click to follow a linked list. ChipView-196 provides a high level view to let you debug



your code in the same language you wrote it in, and a low level view for those times when you need to see the assembly level code.

MICROCONTROLLERS SUPPORTED:

8xC196Kx/Mx/Nx, 80296SA

DEVELOPMENT PLATFORMS:
Windows 3.1, Windows 95, Windows NT,
Extended DOS

AVAILABILITY:
Now

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