AVS51 High-Level Simulator

- High Level Language Debugging
- Precise T-State Simulation
- Performance Analyzer
- Conditional Breakpoints
- Disassembler
- External Environment Simulation
- Auto-Watch Window
- Batch Processor For Automated Testing
- Context Sensitive Help
- Patch Assembler
- Fully Configurable Interface

You cannot crack open an Intel 8051 and watch your program execute. The next closest thing is to debug your code with the AVS51 Simulator. All internal peripherals are exactly simulated and synchronized with the T-states of the processor.

The interface of the AVS51 is completely flexible. You can set up any macros or screen layouts and save them in configuration files. These files can be swapped with a single keystroke. The interface includes an Auto Watch window that updates the variables being watched based on the code in the source window.

The performance analyzer will automatically display the amount and percentage of time spent in each function. The analysis can be further refined by defining functions as library code, adding the time to the function that called it, or excluding functions that sit in delay loops waiting for interrupt.

You can even simulate your external signals and peripherals with exact timing. Simply setup a command file including delays in model time. Several command files can run simultaneously, simulating peripherals, logging output, or even automating your testing.

The AVS51 is completely compatible compilers from Avocet, Franklin, Keil, Archimedes, IAR, and Intel. It can handle source code in C, assembly, and even PLM51. It can be upgraded to include our AVC51 ANSI C Compiler and assembler or packaged with tools from Metalink, Cactus Logic, EE Tools, Softaid, and Tribal Microsystems.

MICROCONTROLLERS SUPPORTED:
8xC3x, 8xC5x, 8xC5xFx, 8xC5xGx, 8xL5x

DEVELOPMENT PLATFORMS:
Windows*, PC DOS

AVAILABILITY:
Now

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