

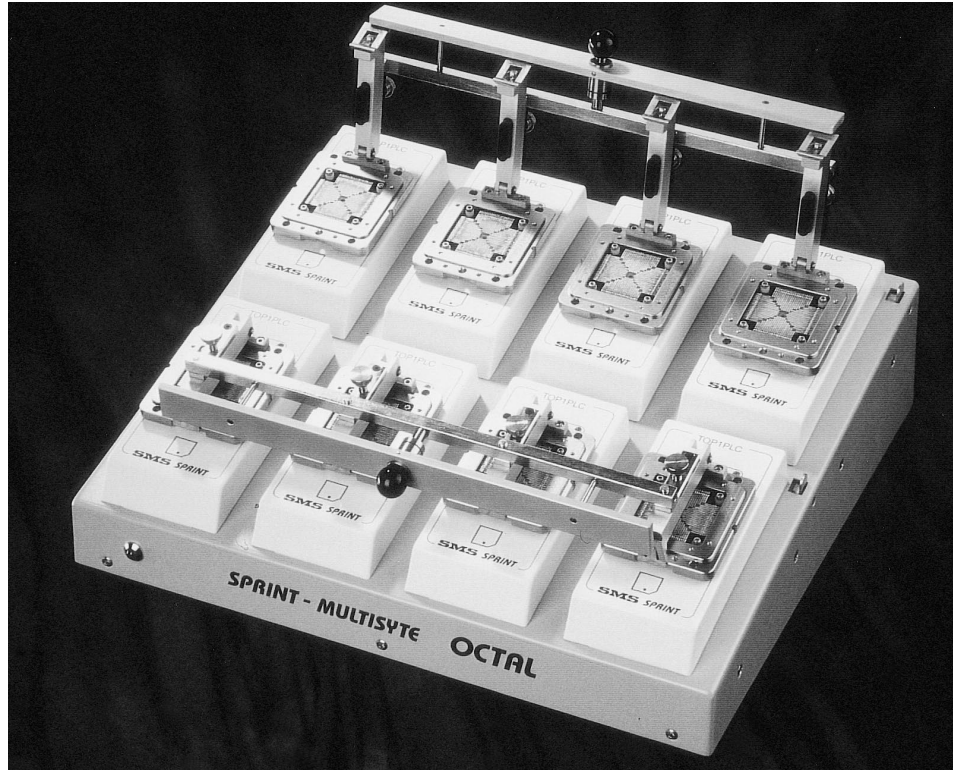
SPRINT MULTISYTE

- Supports All Intel Microcontrollers
- Optimized Programming Speeds For Microcontrollers
- Universal GANG Programming of More Than 5000 Devices
- Available in Three Basic Configurations
- GANG or SWAP Mode For Highest Throughputs
- Testvector Support Up to 84 Pins
- Variable TOPs For Universal and Flexible Solutions
- 48 DIP For Universal DIP Support From 8 to 48 Pins
- Original Vendor Algorithms
- Algorithm Updates Via BBS or Diskette

The SPRINT MULTISYTE is a unique concept in GANG programming. Based on the universal programming design of the SPRINT universal programmer, the MULTISYTE provides GANG programming for the entire spectrum of programming devices including FLASH, EPLDs, FPGAs, E(E)PROMs and Micros up to and beyond 84 pins. The concept of the MULTISYTE is to put multiple electrically isolated programming electronics under the same controller. Each programming site has its own universal pin drivers that can be controlled separately or in parallel.

With a full function pin driver for every pin of the device, the SPRINT MULTISYTE not only supports today's programmable devices, it is ready for tomorrow's devices as well.

The MULTISYTES are the only programmers available that support the parallel universal programming of thousands of devices in one single hardware. Package variations can be adapted by exchanging the "TOPs" or mounting adapters. Both solutions are available for all packages up to 288 pins.



The MULTISYTES are the flexible and economic solution for programming requirements from single pieces to several thousands per day. They close the gap between single stations and the automated device handlers.

MICROCONTROLLERS SUPPORTED:

87C196Kx, 87C196Mx, 87C196Nx,
87C5x, 87L51Fx, 87C251Sx

DEVELOPMENT PLATFORMS:

PC (Non-Windows), PC (Windows 95)

AVAILABILITY:

Now

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